



# P-Channel 1.8 V (G-S) MOSFET

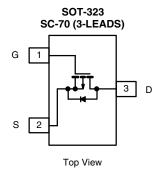
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
	0.280 at V <sub>GS</sub> = - 4.5 V	- 0.92		
- 8	0.380 at V <sub>GS</sub> = - 2.5 V	- 0.79		
	0.530 at V <sub>GS</sub> = - 1.8 V	- 0.67		

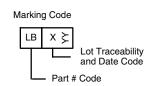
#### **FEATURES**

- TrenchFET® Power MOSFET: 1.8 V
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



**HALOGEN** FREE





Ordering Information: Si1305DL-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	$(T_A = 25  ^{\circ}C,  unlet)$	ess otherwise	noted)		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 8		V
Gate-Source Voltage		V <sub>GS</sub>	± 8		
Continuous Dunin Courset /T 150 90\d	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 0.92	- 0.86	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 0.74	- 0.69	
Pulsed Drain Current		I <sub>DM</sub>	- 3		А
Continuous Diode Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 0,28	- 0.24	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	0.34	0.29	W
	T <sub>A</sub> = 70 °C		0.22	0.19	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maniform Locality Auditoria	t ≤ 5 s	R <sub>thJA</sub>	315	375	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		360	430	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	285	340	

#### Note:

a. Surface mounted on 1" x 1" FR4 board.

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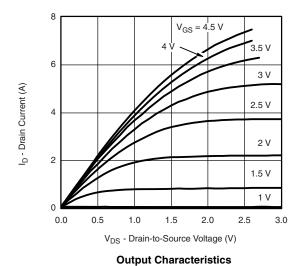
<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min. Typ.		Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 0.45			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zana Oata Wallana Busin Oamant	1	V <sub>DS</sub> = -8 V, V <sub>GS</sub> = 0 V	- 1		- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 5	- μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 3			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1 A		0.230	0.280		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -0.5 \text{ A}$		0.315	0.380	Ω	
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 0.3 A		0.440	0.530		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 1 A		3.5		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 0.3 A, V <sub>GS</sub> = 0 V			- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			2.6	4	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 4 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 1 A		0.6			
Gate-Drain Charge	$Q_{gd}$			0.5			
Turn-On Delay Time	t <sub>d(on)</sub>			8	15		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 4 V, $R_L$ = 4 $\Omega$		55	80		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 4.5 V, $\text{R}_\text{g}$ = 6 $\Omega$		17	25	ns	
Fall Time	t <sub>f</sub>			12	20		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1 A, dI/dt = 100 A/μs		27	45		

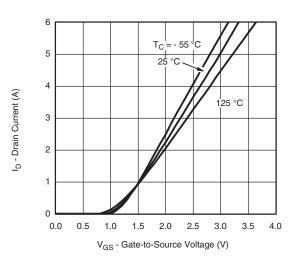
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

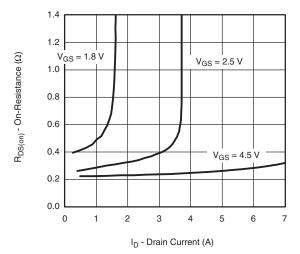




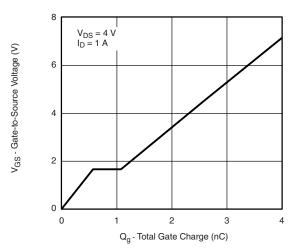
**Transfer Characteristics** 



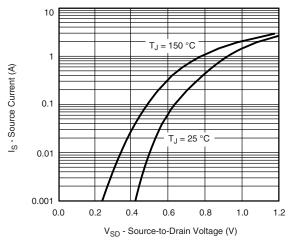
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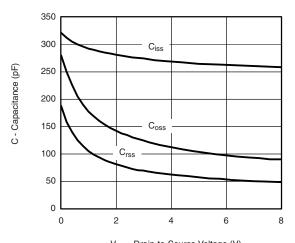
#### On-Resistance vs. Drain Current



**Gate Charge** 

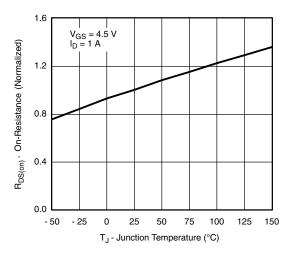


Source-Drain Diode Forward Voltage

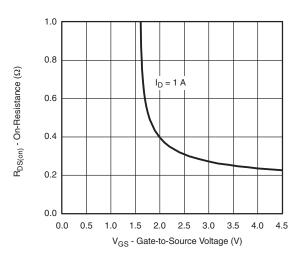


V<sub>DS</sub> - Drain-to-Source Voltage (V)

#### Capacitance



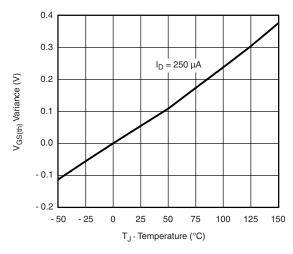
On-Resistance vs. Junction Temperature

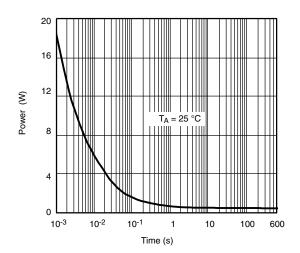


On-Resistance vs. Gate-to-Source Voltage

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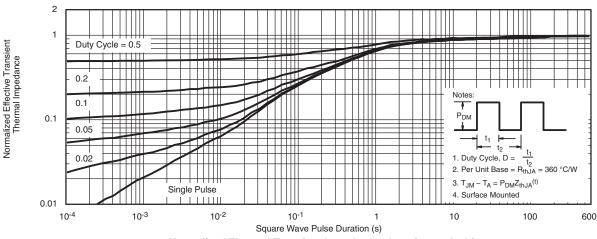
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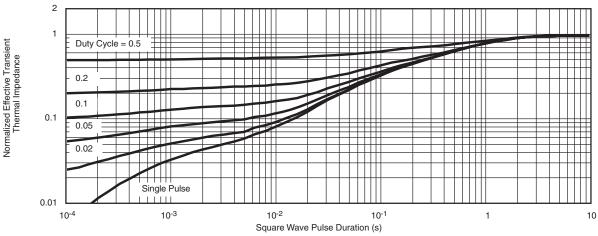


Threshold Voltage

Single Pulse Power



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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