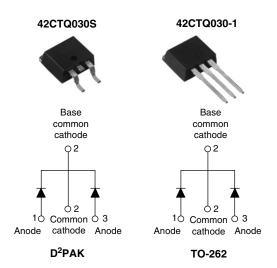


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 20 A				
V_{R}	30 V			

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- · Very low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	Α		
V _{RRM}		30	V		
I _{FSM}	$t_p = 5 \mu s sine$	1100	Α		
V _F	20 Apk, T _J = 125 °C (per leg)	0.38	V		
T _J	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	AMETER SYMBOL		UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg	I _{F(AV)} 50 % duty cycle at T _C = 121 °C, rectangular waveform		20		
See fig. 5 per device	I _{F(AV)}	50 % duty cycle at 1 _C = 121 °C, rectangular wavelonn		40	A
Maximum peak one cycle non-repetitive surge current per leg	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	110	
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse		360	
Non-repetitive avalanche energy per leg E _{AS}		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3 \text{A}, L = 2.90 \text{mH}$		13	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	А

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42CTQ030S/42CTQ030-1

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.48	
		40 A		0.57	V
		20 A	T _J = 125 °C	0.38	
		40 A		0.51	
Maximum reverse leakage current per leg	eg , (1)	T _J = 25 °C	V _R = Rated V _R	3	mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C		183	IIIA
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.22	V
Forward slope resistance	r _t			6.76	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		2840	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	ge	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance junction to case per leg	,	D	DC operation	2.0	
Maximum thermal resistance junction to case per package	*	R_{thJC}		1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	R _{thCS} Mounting surface, smooth and greased (Only for TO-262)		
Approximate weight				2	g
				0.07	oz.
Mounting torque -	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Madding daying			Case style D ² PAK	42CTQ0	30S
Marking device			Case style TO-262	42CTQ0	30-1



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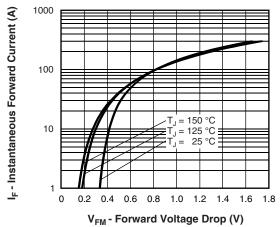


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

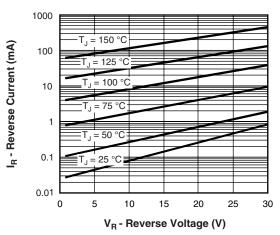


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

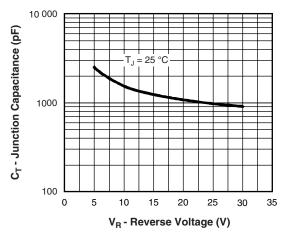


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

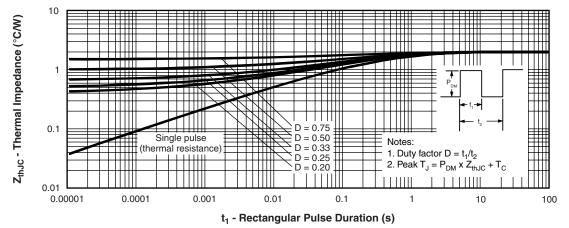
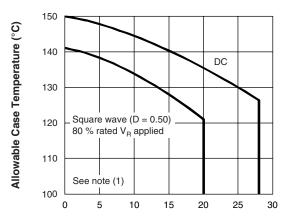


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

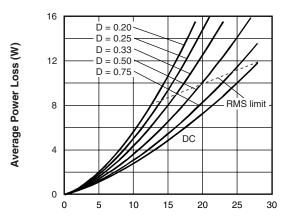
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 $I_{F(AV)}$ - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics (Per Leg)

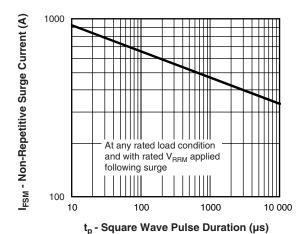


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

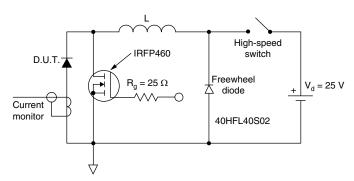


Fig. 8 - Unclamped Inductive Test Circuit

Note

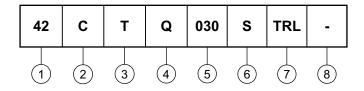
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (40 A)

2 - Circuit configuration:

C = Common cathode

3 - T = TO-220

4 - Schottky "Q" series

5 - Voltage rating (030 = 30 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008		
Packaging information	http://www.vishay.com/doc?95032		

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