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Premstaetten, November 15, 2018

Product Change Notification PCN34-2018 Performance limitation AS3955A

Dear valueable customer,

All EEPROM memories support a limited number of endurance cycles. We observed during long term tests that the number of EEPROM endurance cycles supported by the AS3955A can be lower than originally targeted. We have updated our datasheet and specifications to reflect the correct number of EEPROM endurance cycles accordingly. The number of EEPROM Read cycles is unaffected and remains unlimited. Also the number of Write or Read cycles to the 32 byte Buffer memory is unaffected.

1. Limitation of No. of EEPROM Endurance cycles:

Particularly at high temperature the number of EEPROM endurance cycles supported is reduced. From long term tests we can guarantee 100 EEPROM endurance cycles over the operating temperature range: junction temperature (-40 to 125C) and process.

Bankverbindungen/ Bankaccounts UniCredit Bank Austria AG, Graz IBAN EUR AT28 1200 0763 1316 1100 BIC BKAUATWW IBAN USD AT60 1200 0763 1316 1106

Firmenbuchgericht Graz Firmenbuch Nr. FN 34109k **DVR** 0420352 **UID/VAT** ATU 28560205



## **Affected Products:**

material ID	product description
501610006	AS3955A-ATDM-I4 MLPD10 LF T&RDP
501610010	AS3955A-ATDM-S4 MLPD10 LF T&RDP
501610019	AS3955A-AWLT-S4 WLP LF T&R
501610020	AS3955A-ASWF-S4 SW
501610021	AS3955A-AWLM-S4 WLP LF T&R
501610025	AS3955A-AWLT-I4 WLP LF T&R
501610026	AS3955A-AWLM-I4 WLP LF T&R
501610027	AS3955A-ASWF-I4 SW
501610033	AS3955A-ATWT-S4 WLP LF T&R
501610034	AS3955A-ATWM-S4 WLP LF T&R
501610038	AS3955A-ATWT-I4 WLP LF T&R
501610039	AS3955A-ATWM-I4 WLP LF T&R

Timing of change:

Immediate

We apologize for any inconvenience caused by ams AG. If you do have further questions please do not hesitate to contact us.

Best regards,

Defluppe

Dr. Dietmar Gleispach ams AG Director Operations EAS