LI1806E800R-10

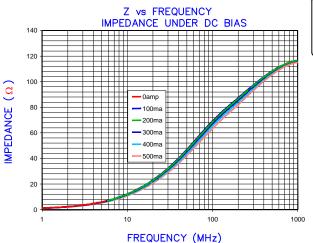
PHYSICAL DIMENSIONS:

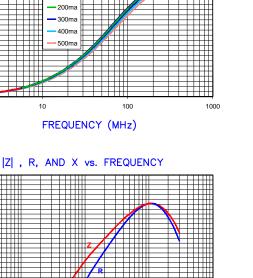
A 4.50 [.177] ± 0.25 [.010]

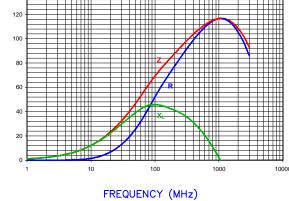
B 1.60 [.063] ± 0.25 [.010]

C 1.60 [.063] ± 0.25 [.010]

D 0.51 [.020] ⁺ 0.25 [.010]

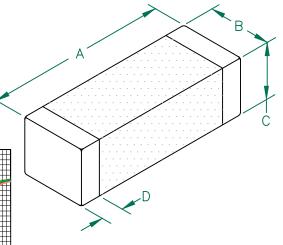






AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3298

IMPEDANCE

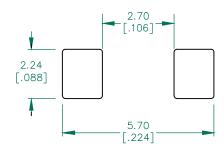


ELECT	ELECTRICAL CHARACTERISTICS:									
Z @ 100MHz (Ω)		DCR (Ω)	Rated Current							
Nominal	80									
Minimum	60									
Maximum	100	0.30	500 mA							

NOTES: UNLESS OTHERWISE SPECIFIED

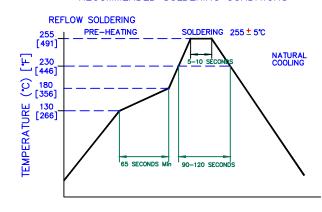
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2,000 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS





_										
Г	DIMENSIONS ARE IN mm [INCHES].			This print is the property of Lair						
					Tech, and is loaned in confidence subject to return upon request ar			_	•	J"
	F	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU		e	[¶] Laird			
	E		11/05/08	JRK	rights to design or invention are		_			
	D	UPDATE COMPANY LOGO	07/24/08	JRK	reserved.					
Γ	O	CORRECT REF # FROM 3289 TO 3298	07/22/05	JRK		ľ	REV PART T		-	DRAWN BY:
Γ	В	CORRECT LANDPATTERN DIM AND TEMP	04/14/03	JRK	LI1806E800R-10		F	CO-FIRE		JRK
L	_	IN REFLOW SOLDERING, ADD DC CURVE	.,,		DATE: 08/07/02	SCAL	E: N	TS SHEET:		
	Α	ORIGINAL DRAFT	08/07/02	JRK		TOOL		13		
	REV	DESCRIPTION	DATE	INT				-	1	of 1