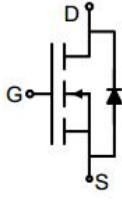
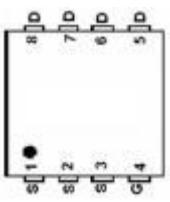
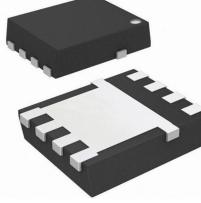


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The G30N03D3 uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 30V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 30A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 7mΩ</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 4.5V</math>) &lt; 12mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>pin assignment</p>  <p>DFN3*3-8L</p>		
Device	Package	Marking	Packaging
G30N03D3	DFN3*3-8L	G30N03	5000pcs/Reel

<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Continuous Drain Current	$I_D$	30	A
Pulsed Drain Current (note1)	$I_{DM}$	120	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	24	W
Single pulse avalanche energy (note2)	EAS	49	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

<b>Thermal Resistance</b>			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	24	°C/W
Maximum Junction-to-Case	$R_{thJC}$	5.2	°C/W

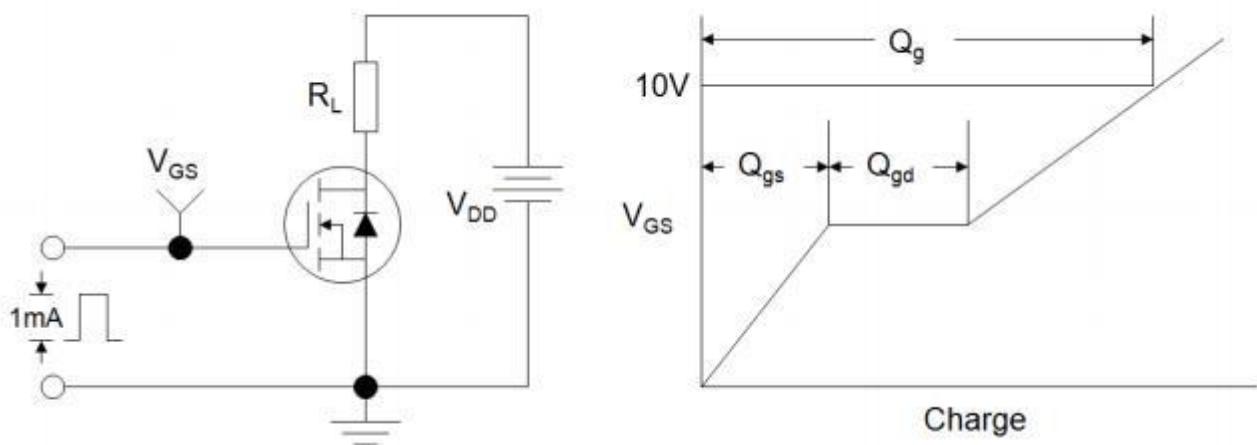
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}$	--	--	$\pm 100$	$\text{nA}$
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.4	2.5	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$	--	5	7	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 15\text{A}$	--	7	12	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = 5\text{V}, I_D = 20\text{A}$	--	40	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1.0\text{MHz}$	--	1260	--	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		--	230	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	185	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 15\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}$	--	13	--	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		--	2.2	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	2.6	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 15\text{V}, I_D = 20\text{A}, R_G = 3\Omega$	--	5	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	3	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	20	--	
Turn-off Fall Time	$t_f$		--	3	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_s$	$T_c = 25^\circ\text{C}$	--	--	30	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 20\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$ $di/dt = -500\text{A}/\mu\text{s}$	--	15.2	--	$\text{nC}$
Reverse Recovery Time	$T_{\text{rr}}$		--	12.6	--	ns

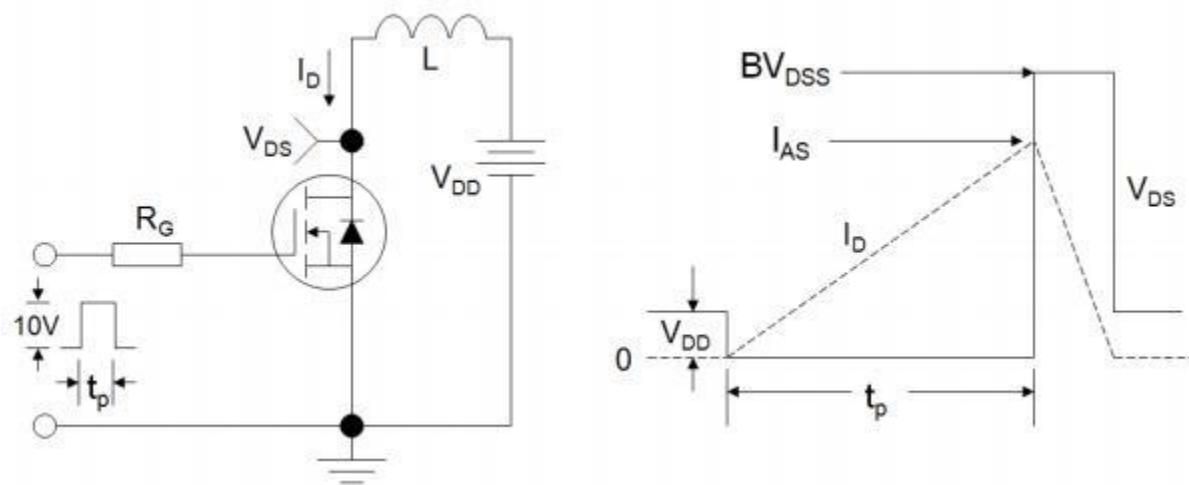
**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_j=25^\circ\text{C}$ ,  $V_{\text{DD}}=30\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$
3. Identical low side and high side switch with identical  $R_g$

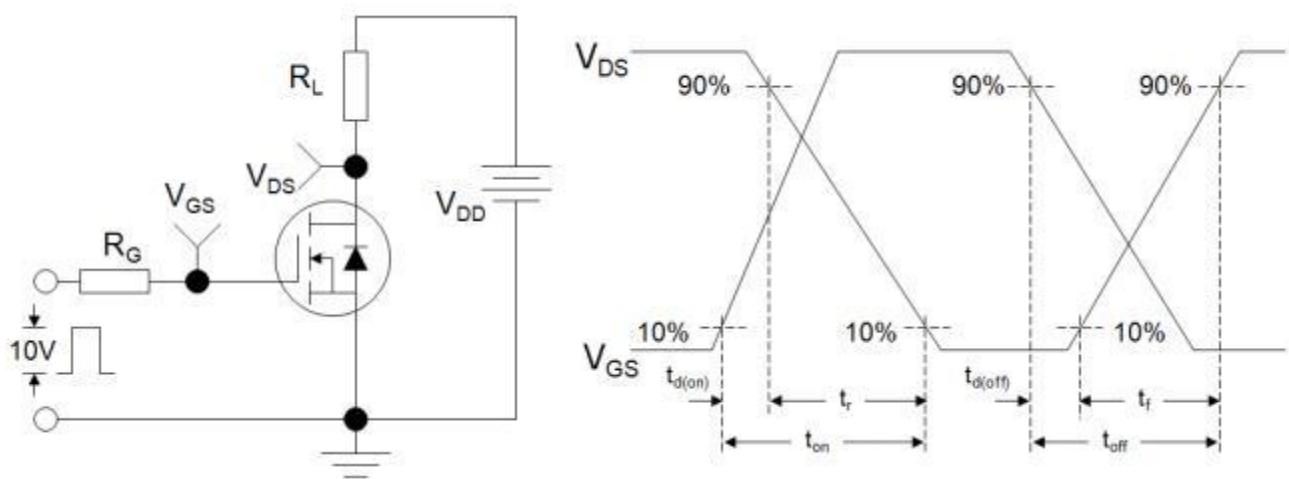
**Gate Charge Test Circuit**



**EAS Test Circuit**

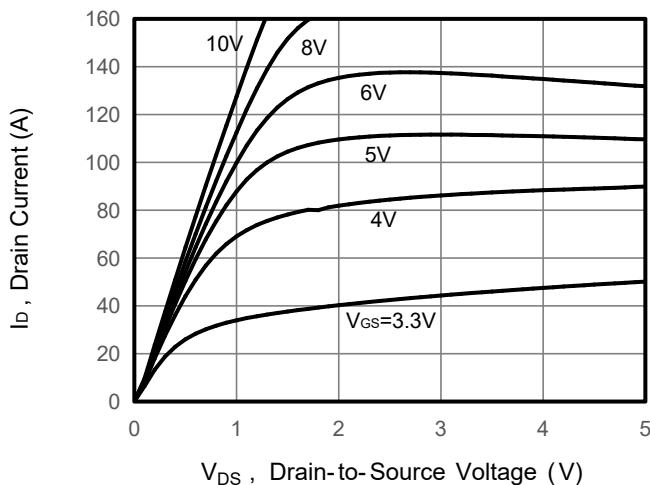


**Switch Time Test Circuit**

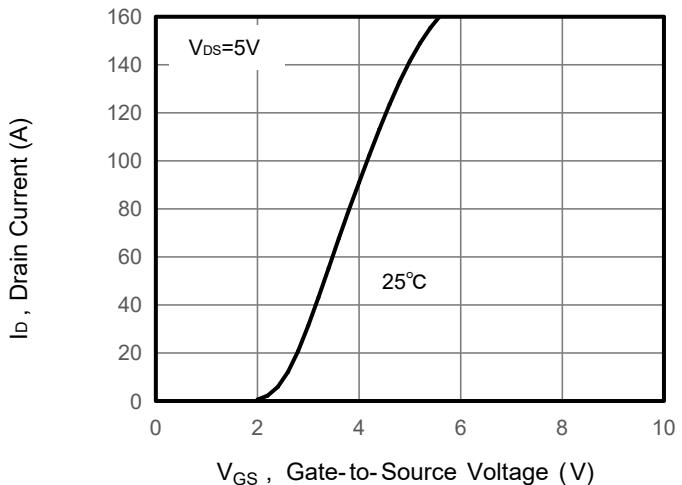


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

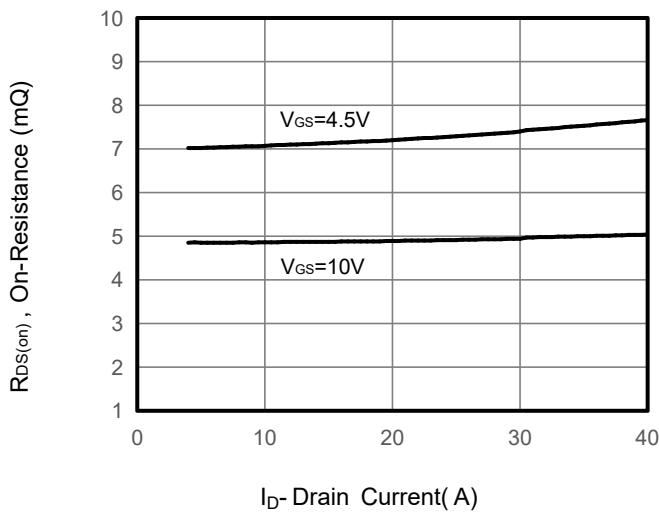
**Figure 1 . Output Characteristics**



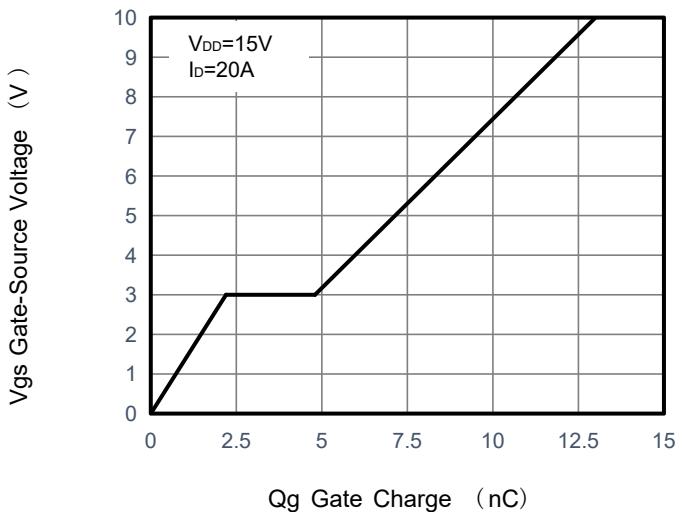
**Figure 2 . Transfer Characteristics**



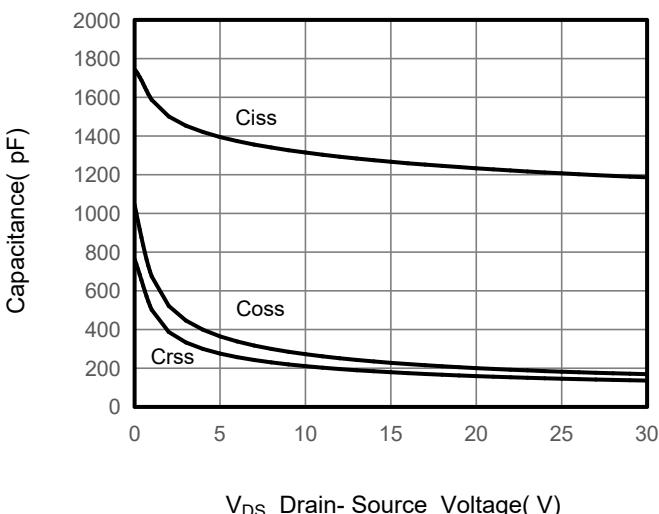
**Figure 3.Rdson-Drain Current**



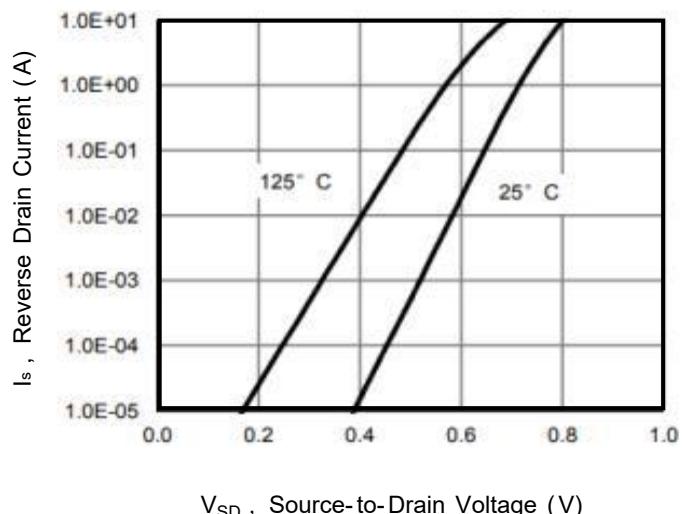
**Figure 4. Gate Charge**



**Figure 5 . Capacitance vs Vds**

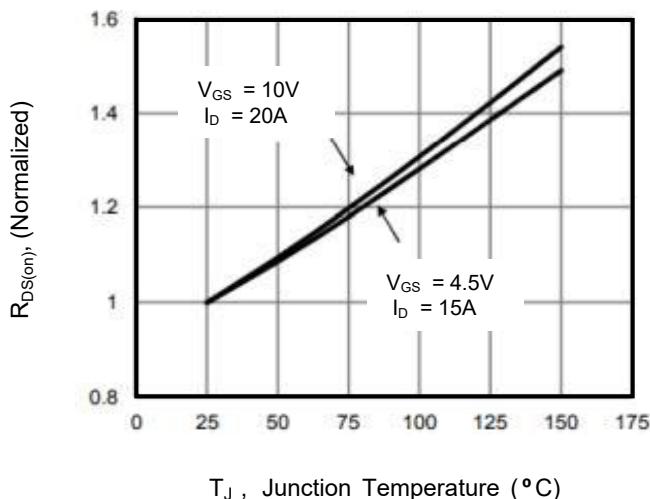


**Figure 6 . Source-Drain Diode Forward**

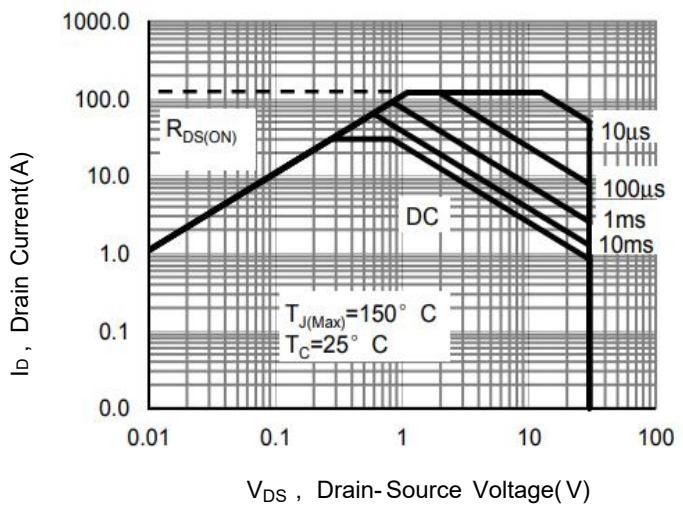


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

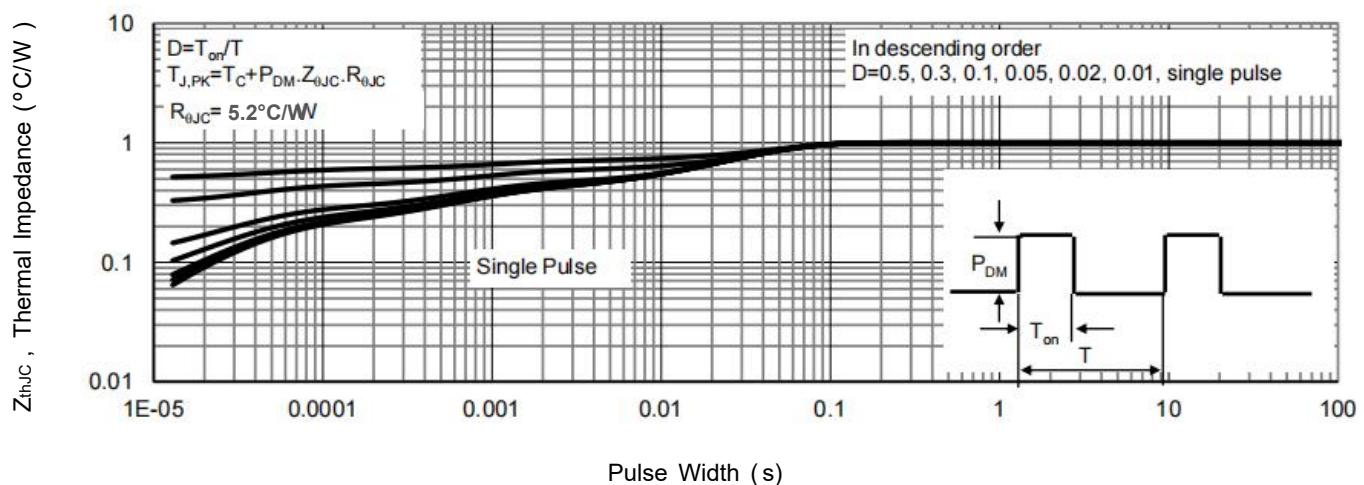
**Figure 7. Drain-Source On-Resistance**

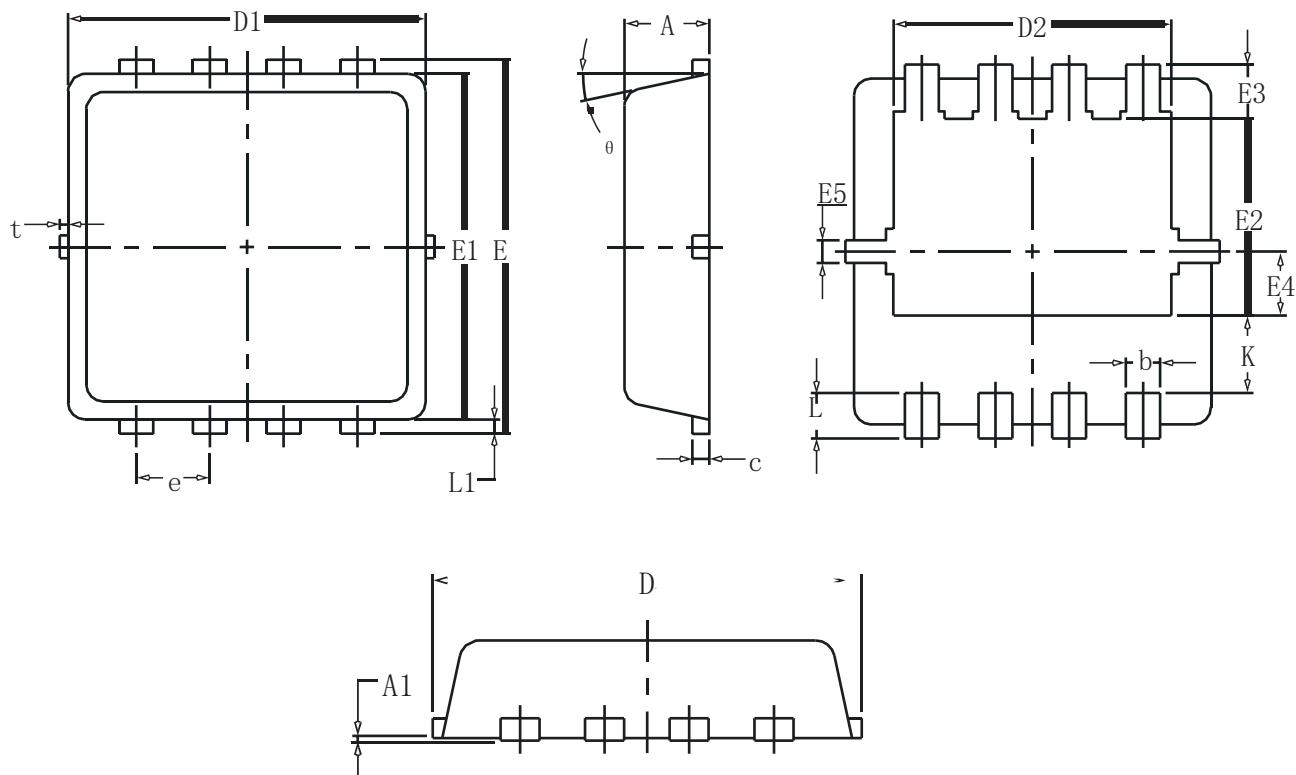


**Figure 8. Safe Operation Area**



**Figure 9 . Normalized Maximum Transient Thermal Impedance**



**DFN3x3-8L Package Information**


SYMBOL	COMMON		
	MM		
	MIN	NOM	MAX
A	0.70	0.75	0.85
A1	-	-	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
θ	10°	12°	14°