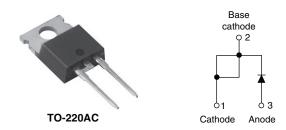
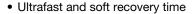


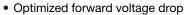
Ultrafast Rectifier, 15 A FRED Pt®

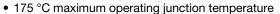


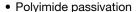
PRODUCT SUMMARY					
Package	TO-220AC				
I _{F(AV)}	15 A				
V _R	1200 V				
V _F at I _F at 125 °C	2.25 V				
t _{rr}	44 ns				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES









• Rugged design

Good thermal performance

AEC-Q101 qualified

• Meets JESD 201 class 2 whisker test

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ROHS COMPLIANT HALOGEN FREE

DESCRIPTION / APPLICATIONS

Ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, recovery time, and soft recovery. Polyimide passivated, planar structure and the platinum doped life time control guarantee, ruggedness, reliability characteristics, and solid value proposition for efficiency and thermal performance.

These devices are intended for use in boost stage in the AC/DC section of SMPS, high frequency output rectification of battery charger, inverters of solar inverters, or as freewheeling diodes in motor drive.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Repetitive peak reverse voltage	V _{RRM}		1200	V			
Average rectified forward current	I _{F(AV)}	T _C = 115 °C, D = 0.50	15				
Non-repetitive peak surge current	I _{FSM}	$T_C = 25$ °C, $t_p = 10$ ms, sine wave	150	Α			
Repetitive peak forward current	I _{FRM}		30				
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MA			MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 250 μA	1200	-	-		
Faculated well-are	V _F	I _F = 15 A	-	2.3	2.78	'8 V	
Forward voltage		I _F = 15 A, T _J = 125 °C	-	2.25	2.7		
Boyara lagkaga ayrrant	I _R	$V_R = V_R$ rated	-	-	80		
Reverse leakage current		$T_J = 125 ^{\circ}C$, $V_R = V_R$ rated	-	-	150	μA	
Junction capacitance	C _T	V _R = 200 V	-	13	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	ı	8.0	-	nH	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt = 10$	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		44	-	
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 100 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	167	-	ns
		T _J = 125 °C		-	248	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	6	-	A
		T _J = 125 °C		-	9	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	507	-	nC
		T _J = 125 °C		-	1110	-	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Thermal resistance, junction to case	R_{thJC}		-	1.1	1.3			
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	54	60	°C/W		
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	0.4			
NA/a:lat			-	0.2	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C		
Marking device		Case style: TO-220AC	15ETU12H					

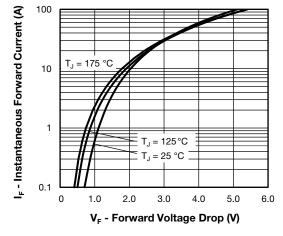


Fig. 1 - Typical Forward Voltage Drop Characteristics

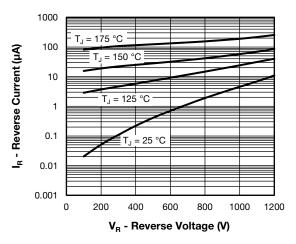


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

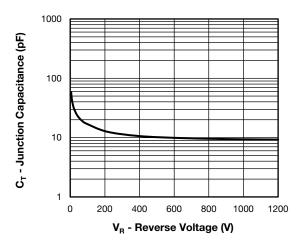


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

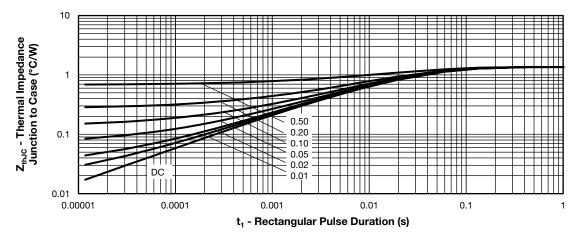


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

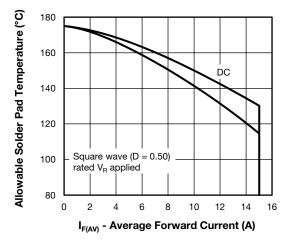


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

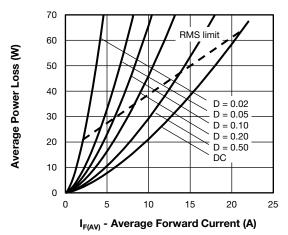


Fig. 6 - Forward Power Loss Characteristics

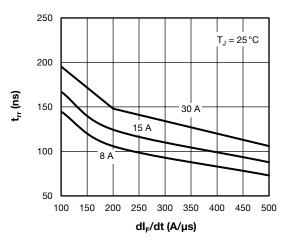


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

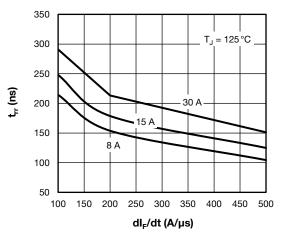


Fig. 8 - Typical Reverse Recovery Time vs. dl_F/dt

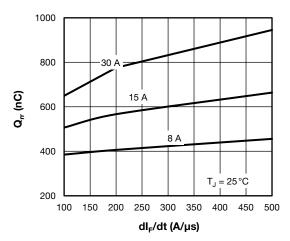


Fig. 9 - Typical Stored Charge vs. dI_F/dt

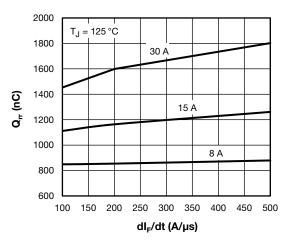


Fig. 10 - Typical Stored Charge vs. dl_F/dt

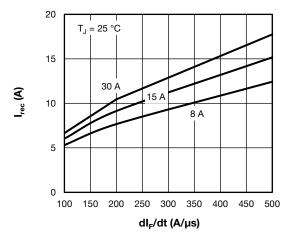


Fig. 11 - Typical Reverse Current vs. dI_F/dt

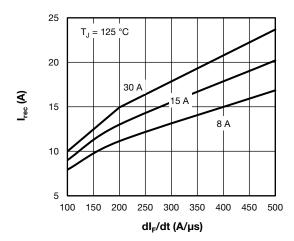
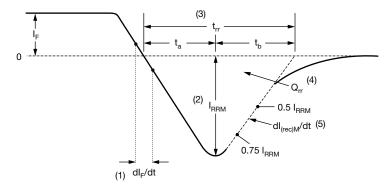


Fig. 12 - Typical Reverse Current vs. dl_F/dt





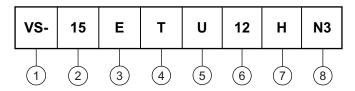
- (1) dl_E/dt rate of change of current through zero crossing
 - and I_{RRM}
- (2) I_{RRM} peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (5) dI_{(rec)M}/dt peak rate of change of current during t_b portion of t_{rr}

(4) Q_{rr} - area under curve defined by t_{rr}

Fig. 13 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- Current rating (15 = 15 A)
- E = single diode
- Package:

T = TO-220

- U = ultrafast recovery
- Voltage rating (12 = 1200 V)
- H = AEC-Q101 qualified
- Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

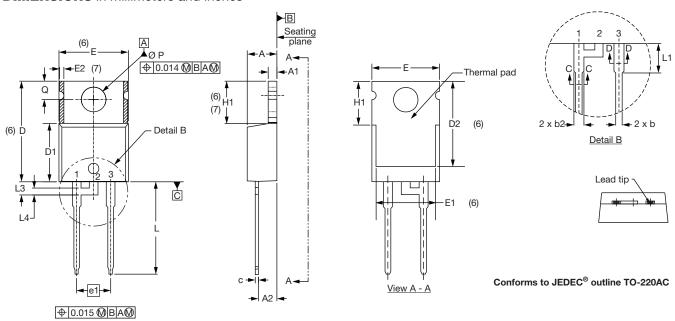
ORDERING INFORMATION (Example)							
PREFERRED P/N	RRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-15ETU12HN3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95221</u>					
Part marking information	www.vishay.com/doc?95068				



TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOIES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
E	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
e1	4.88	5.28	0.192	0.208	
H1	5.84	6.86	0.230	0.270	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- $^{(7)}$ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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Vishay

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