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## ATA6662C Errata and Data Sheet Clarifications

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The functionality of the automotive qualified ATA6662C device you have received is described in the Device Data Sheet 4916S-AUTO-09/14 except for the anomalies described in this document.

There are no silicon errata issues for the ATA6662C. This document contains just some detailed information in addition to the ATA6662C data sheet 4916S-AUTO-09/14.

There is no change in the product itself.

### 1.0 SILICON ISSUES

None.

### 2.0 DATA SHEET CLARIFICATIONS

In the Device Data Sheet 4916S-AUTO-09/14, the following clarifications and supplements should be noted:

#### 2.1 Package Drawing

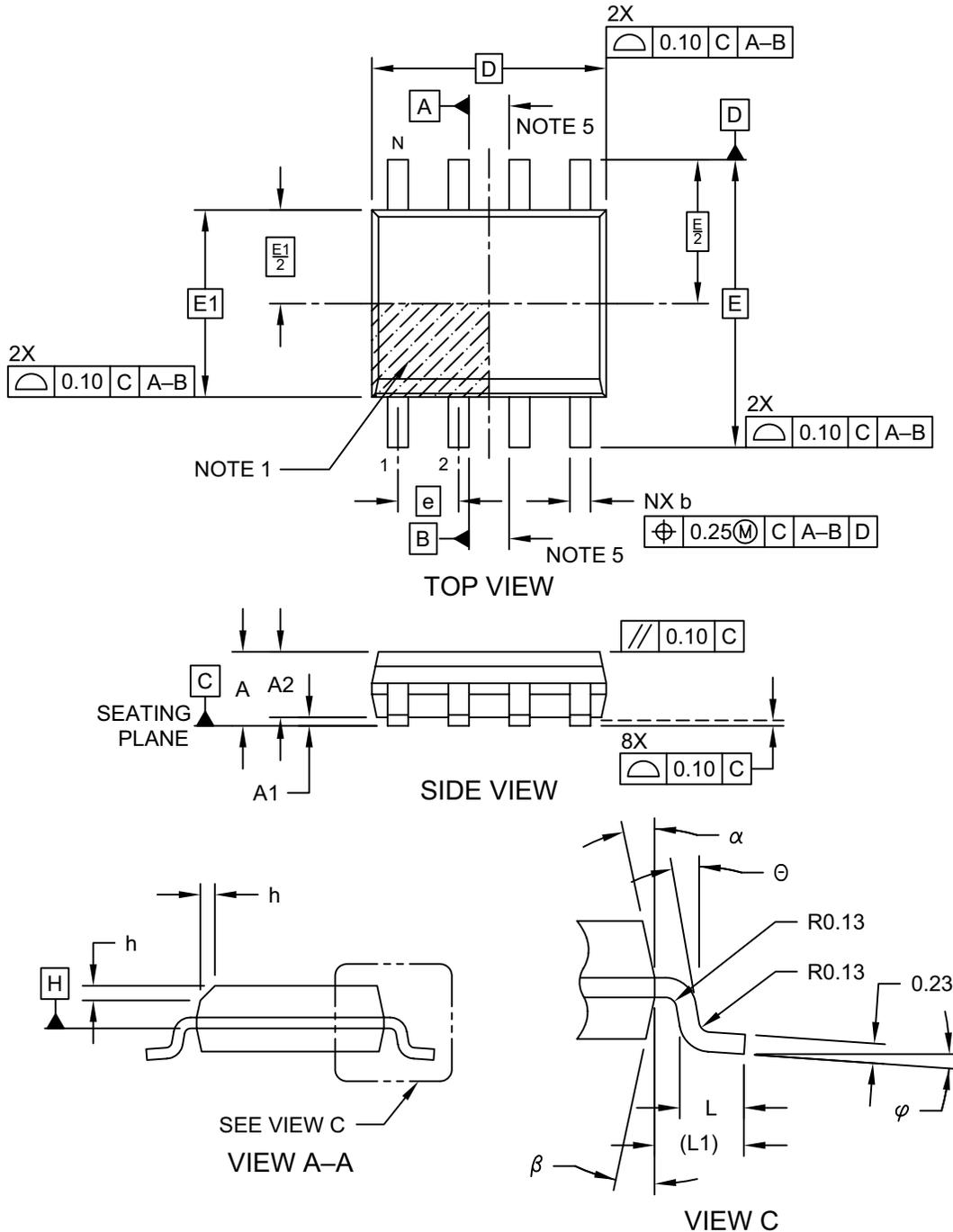
The package drawings have been updated. They have been converted from the former Atmel style to the Microchip Format without changing any dimensions.

The new package drawings are depicted on pages 2, 3 and 4:

# ATA6662C

## 8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm (.150 In.) Body [SOIC]

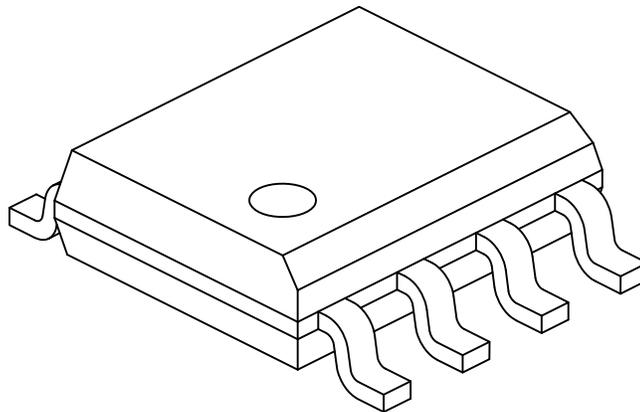
**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Microchip Technology Drawing No. C04-057-SN Rev F Sheet 1 of 2

## 8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm (.150 In.) Body [SOIC]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Pins	N	8		
Pitch	e	1.27 BSC		
Overall Height	A	-	-	1.75
Molded Package Thickness	A2	1.25	-	-
Standoff §	A1	0.10	-	0.25
Overall Width	E	6.00 BSC		
Molded Package Width	E1	3.90 BSC		
Overall Length	D	4.90 BSC		
Chamfer (Optional)	h	0.25	-	0.50
Foot Length	L	0.40	-	1.27
Footprint	L1	1.04 REF		
Foot Angle	$\phi$	0°	-	8°
Lead Thickness	c	0.17	-	0.25
Lead Width	b	0.31	-	0.51
Mold Draft Angle Top	$\alpha$	5°	-	15°
Mold Draft Angle Bottom	$\beta$	5°	-	15°

**Notes:**

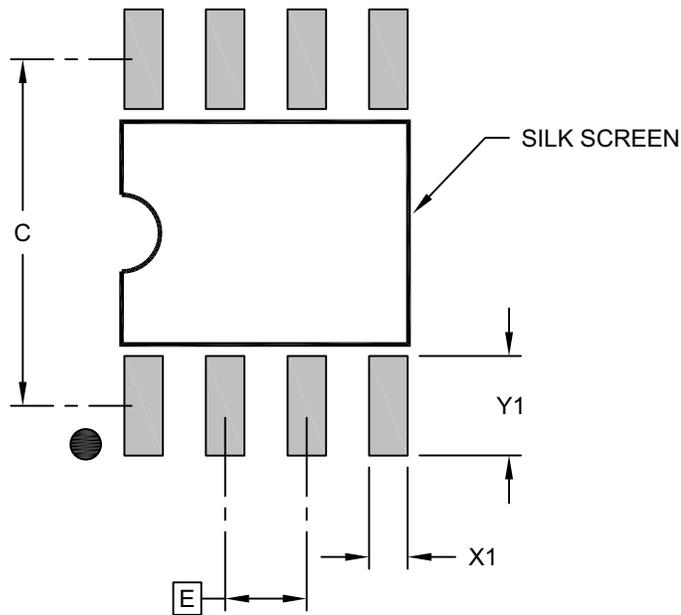
- Pin 1 visual index feature may vary, but must be located within the hatched area.
- § Significant Characteristic
- Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm per side.
- Dimensioning and tolerancing per ASME Y14.5M  
 BSC: Basic Dimension. Theoretically exact value shown without tolerances.  
 REF: Reference Dimension, usually without tolerance, for information purposes only.
- Datums A & B to be determined at Datum H.

Microchip Technology Drawing No. C04-057-SN Rev F Sheet 2 of 2

# ATA6662C

## 8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm (.150 In.) Body [SOIC]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	1.27 BSC		
Contact Pad Spacing	C		5.40	
Contact Pad Width (X8)	X1			0.60
Contact Pad Length (X8)	Y1			1.55

**Notes:**

1. Dimensioning and tolerancing per ASME Y14.5M

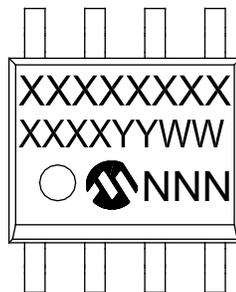
BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-2057-SN Rev F

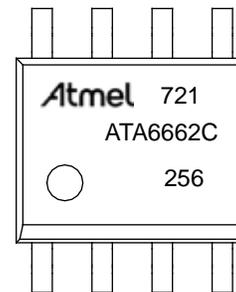
## 2.2 Package Marking Information

In the ATA6662C data sheet 4916S-AUTO-09/14 there is no information regarding the Package Marking. Therefore, this information is listed here:

8-Lead SOIC



Example



<b>Legend:</b>	XX...X	Customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week '01')
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator (e3) can be found on the outer packaging for this package.
<b>Note:</b>	In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.	

# ATA6662C

## 2.3 Product Information System

The Product Information System is also not available in the existing data sheet:

### PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

<u>PART NO.</u>	<u>XX</u>	<u>[X1]<sup>(1)</sup></u>	<u>[X1]<sup>(2)</sup></u>	<b>Example:</b>
Device	Package	Tape and Reel Option	Package Directives Classification	
<b>Device:</b>	ATA6662C			a) ATA6662C-GAQW = 8-Lead SOIC, Automotive AEC-Q100 Qualified, Tape and Reel package according to RoHS
<b>Package:</b>	GA = 8-Lead SOIC			<b>Note 1:</b> Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.  <b>2:</b> RoHS compliant, maximum concentration value of 0.09% (900 ppm) for Bromine (Br) and Chlorine (Cl), and less than 0.15% (1500 ppm) total Bromine (Br) and Chlorine (Cl) in any homogeneous material. Maximum concentration value of 0.09% (900 ppm) for Antimony (Sb) in any homogeneous material.
<b>Tape and Reel Option:</b>	Q = 330 mm diameter Tape and Reel <sup>(1)</sup>			
<b>Package Directives Classification:</b>	W = Package according to RoHS <sup>(2)</sup>			

## 2.4 Trademarks Page

The information on [page 9](#) is also not available in the existing data sheet:

## APPENDIX A: DOCUMENT REVISION HISTORY

### Rev. A (November 2020)

- Initial Release of this Document: Errata released as a separate document. This Errata completes the Device Data Sheet 4916S-AUTO-09/14 with the following information:
  - Updated Package Drawing for the 8-Lead SOIC package
  - Adding Package Marking Information
  - Product Information System; (replacing Ordering Information)
  - Updated Microchip Trademarks page

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**Note the following details of the code protection feature on Microchip devices:**

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- Microchip believes that its family of products is secure when used in the intended manner and under normal conditions.
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