TOSHIBA Multi-Chip Device Silicon N Channel MOS Type (U-MOS III) / Schottky Barrier Diode

## TPCF8A01

# Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: R<sub>DS</sub> (ON) = 38 mÙ (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 5.4 \text{ S (typ.)}$
- Low leakage current: IDSS = 10 iA (max) (VDS = 20 V)
- Enhancement-model:  $V_{th} = 0.5$  to 1.2 V ( $V_{DS} = 10$  V,  $I_{D} = 200$  iA)
- Low forward voltage: V<sub>FM</sub> = 0.46V(typ.)

## **Maximum Ratings**

### MOSFET (Ta = 25°C)

Cha	aracteristics	Symbol	Rating	Unit		
Drain-source volta	$V_{DSS}$	20	V			
Drain-gate voltage	$V_{DGR}$	20	V			
Gate-source voltage	$V_{GSS}$	±12	V			
Drain current	DC	(Note 1)	I <sub>D</sub>	3	Α	
	Pulse	(Note 1)	$I_{DP}$	12		
Single pulse avala	Single pulse avalanche energy (Note 4)			1.46	mJ	
Avalanche current			I <sub>AR</sub>	1.5	Α	
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3b, 5)			E <sub>AR</sub>	0.11	mJ	

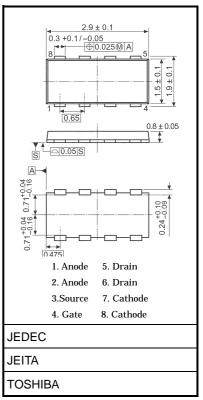
#### SBD (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$	20	٧
Average forward current (Note 2a, 6)	I <sub>F(AV)</sub>	1.0	Α
Peak one cycle surge forward current (non-repetitive)	I <sub>FSM</sub>	7(50Hz)	Α

#### **Maximum Ratings for MOSFET and SBD (Ta = 25°C)**

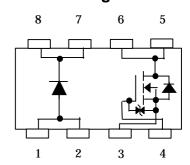
Characteristics		Symbol	Rating	Unit	
Drain power dissipation	Single-device operation (Note 3a)	P <sub>D (1)</sub>	1.35		
(t = 5 s) (Note 2a)	Single-device value at dual operation (Note 3b)	P <sub>D (2)</sub>	1.12		
Drain power dissipation (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	P <sub>D (1)</sub>	0.53	W	
	Single-device value at dual operation (Note 3b)	P <sub>D (2)</sub>	0.33		
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	
			•		

#### Unit: mm

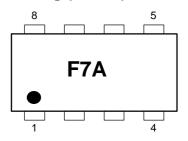


Weight: 0.011 g (typ.)

## **Circuit Configuration**



## Marking (Note 7)



Note: For (Note 1), (Note 2), (Note 3), (Note 4), (Note 5), (Note 6) and (Note 7), please refer to the next page.

#### Thermal Characteristics for MOSFET and SBD

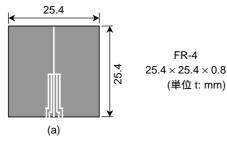
Charac	Symbol	Max	Unit		
Thermal resistance, channel to ambient	Single-device operation (Note 3a)	R <sub>th (ch-a) (1)</sub>	92.6	°C/W	
(t = 5 s) (Note 2a)	Single-device value at dual operation (Note 3b) Rth (ch-a) (2)		111.6	O/VV	
Thermal resistance, channel to ambient	Single-device operation (Note 3a)	R <sub>th (ch-a) (1)</sub>			
(t = 5 s) (Note 2b)	Single-device value at dual operation (Note 3b)	R <sub>th (ch-a) (2)</sub>	378.8	°C/W	

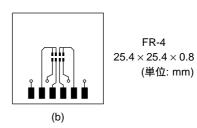
This transistor is an electrostatic sensitive device. Please handle with caution.

Schottky barrier diodes are having large-reverse-current-leakage characteristic compare to the other rectifier products. This current leakage and improper operating temperature or voltage may cause thermal runaway. Please take forward and reverse loss into consideration when you design.

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)





- Note 3: a) The power dissipation and thermal resistance values are shown for a single device (During single-device operation, power is only applied to one device.).
  - b) The power dissipation and thermal resistance values are shown for a single device (During dual operation, power is evenly applied to both devices.).
- Note 4:  $V_{DD} = 16 \text{ V}$ ,  $T_{ch} = 25 ^{\circ}\text{C}$  (initial), L = 0.5 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 1.5 \text{ A}$
- Note 5: Repetitive rating; Pulse width limited by Max. Channel temperature.
- Note 6: Rectangular waveform ( $\alpha = 180^{\circ}$ ),  $V_R = 15V$ .
- Note 7: Black round marking " "locates on the left lower side of parts number marking "F7A" indicates terminal No. 1.

## **Electrical Characteristics (Ta = 25°C)**

## **MOSFET**

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-off curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	_	_	10	μΑ
Drain course bro	akdown voltago	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	20	_	_	V
Drain-source breakdown voltage		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -12 \text{ V}$	8	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_D = 200 \mu\text{A}$	0.5	_	1.2	V
		R <sub>DS (ON)</sub>	$V_{GS} = 2.0 \text{ V}, I_D = 1.5 \text{ A}$	_	62	100	
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$V_{GS} = 2.5 \text{ V}, I_D = 1.5 \text{ A}$	_	50	66	$m\Omega$
		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.5 A	_	38	49	
Forward transfer admittance		Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.5 A	2.7	5.4	_	S
Input capacitance	Э	C <sub>iss</sub>		_	590	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	70	_	pF
Output capacitance		C <sub>oss</sub>		_	85	_	
	Rise time	t <sub>r</sub>	V <sub>GS</sub> <sup>5</sup> V	_	3.0	_	
	Turn-on time	t <sub>on</sub>		_	7.5	_	ns
Switching time	Fall time	t <sub>f</sub>		_	4.4	_	
	Turn-off time	t <sub>off</sub>	$V_{DD} \simeq 10 \text{ V}$ Duty $\leq 1\%$ , $t_w = 10 \mu\text{s}$	_	26	_	
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ 16 V, V <sub>GS</sub> = 5 V,		7.5		
Gate-source charge1		Q <sub>gs1</sub>	$I_D = 3.0 \text{ A}$	_	1.3		nC
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	2.1	_	

**MOSFET Source-Drain Ratings and Characteristics** 

Characterist	ics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I <sub>DRP</sub>	_	_	_	12	Α
Forward voltage (diode)		V <sub>DSF</sub>	I <sub>DR</sub> = 3.0 A, V <sub>GS</sub> = 0 V			-1.2	V

## **SBD**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Peak forward voltage	V <sub>FM(1)</sub>	$I_{FM} = 0.7 A$	_	0.43	_	V
T eak forward voltage	V <sub>FM(2)</sub>	$I_{FM} = 1.0 \text{ A}$	_	0.46	0.49	٧
Repetitive peak reverse current	I <sub>RRM</sub>	V <sub>RRM</sub> = 20 V	_	_	50	μΑ
Junction capacitance	Cj	$V_R = 10 \text{ V, f} = 1 \text{ MHz}$	_	54	_	pF

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