

#### **Features**

- Split Gate Trench MOSFET Technology
- · Excellent Package for Heat Dissipation
- High Density Cell Design for Low R<sub>DS(ON)</sub>
- · Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

#### **Maximum Ratings**

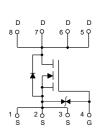
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 17°C/W Junction to Ambient(t≤10S)<sup>(2)</sup>
- Thermal Resistance: 55°C/W Junction to Ambient(Steady-State)<sup>(2)</sup>
- Thermal Resistance: 1.1°C/W Junction to Case(Steady-State)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Volltage	$V_{GS}$	±20	V
Continuous Drain Current	I <sub>D</sub>	85	Α
Pulsed Drain Current <sup>(3)</sup>	I <sub>DM</sub>	340	Α
Total Power Dissipation	P <sub>D</sub>	110	W
Single Pulsed Avalanche Energy <sup>(4)</sup>	E <sub>AS</sub>	400	mJ

#### Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of  $R_{\theta JA}$  is measured with the device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A$  =25°C. The Power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.  $T_J$ =25°C,  $V_{DD}$ =50V, L=2mH,  $I_{AS}$ =20A

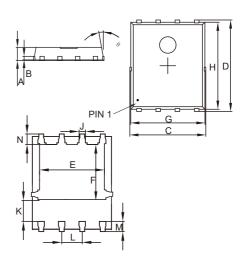
#### **Internal Structure and Marking Code**





# N-CHANNEL MOSFET

## DFN5060



	DIMENSIONS					
DIM	DIM INCHES MIN MAX		MM		NOTE	
DIIVI			MIN	MAX	NOIL	
Α	0.031	0.047	0.80	1.20		
В	0.010		0.254		TYP.	
С	0.193	0.222	4.90	5.64		
D	0.232	0.250	5.90	6.35		
Е	0.148	0.167	3.75	4.25		
F	0.126	0.154	3.20	3.92		
G	0.189	0.213	4.80	5.40		
Н	0.222	0.239	5.65	6.06		
K	0.045	0.059	1.15	1.50		
J	0.012	0.020	0.30	0.50		
L	0.046	0.054	1.17	1.37		
М	0.012	0.028	0.30	0.71		
N	0.016	0.028	0.40	0.71		

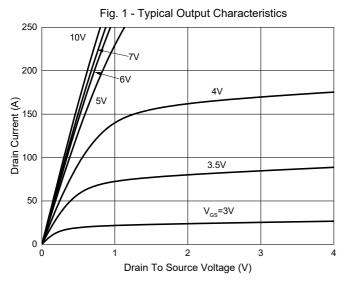


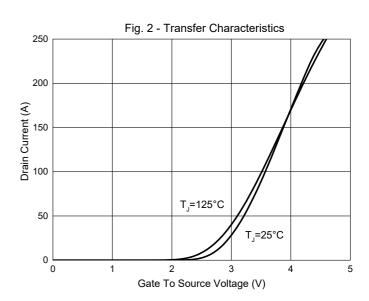
### Electrical Characteristics @ 25°C (Unless Otherwise Specified)

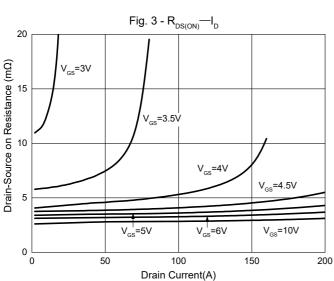
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics					I		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±10	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μA	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.6	2.5	V	
Drain-Source On-Resistance	Б	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		2.9	3.7	mΩ	
	$R_{DS(on)}$	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		3.8	5.0	mΩ	
Gate Resistance	R <sub>G</sub>	f=1MHz, Open drain		2		Ω	
Diode Characteristics	•			•			
Continuous Body Diode Current	Is				85	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A		0.8	1.3	V	
Reverse Recovery Time	t <sub>rr</sub>	- I <sub>F</sub> =20A, dI <sub>F</sub> /dt=500A/μs		41.6		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	- 1 <sub>F</sub> -20A, α1 <sub>F</sub> /α1-300A/μ5		39.8		nC	
Dynamic Characteristics			·	•			
Input Capacitance	C <sub>iss</sub>			4650			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V,f=1MHz		850		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			65			
Total Gate Charge	$Q_g$			70.78			
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =10V,I <sub>D</sub> =25A		16.64		nC	
Gate-Drain Charge	$Q_{gd}$			10.62			
Turn-On Delay Time	t <sub>d(on)</sub>			15.9			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DS</sub> =30V, V <sub>GEN</sub> =10V,		55.2		no	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G=2\Omega$ , $I_{DS}=25A$		57.5		ns	
Turn-Off Fall Time	t <sub>f</sub>			91.3			

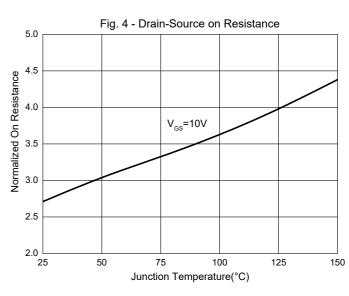


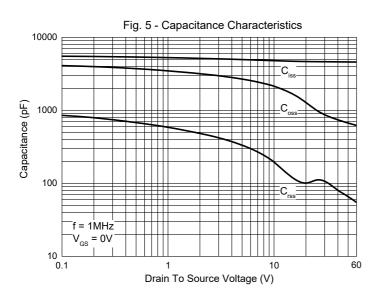
#### **Curve Characteristics**

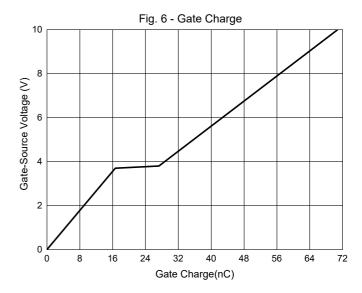






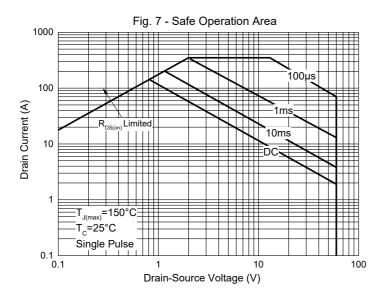


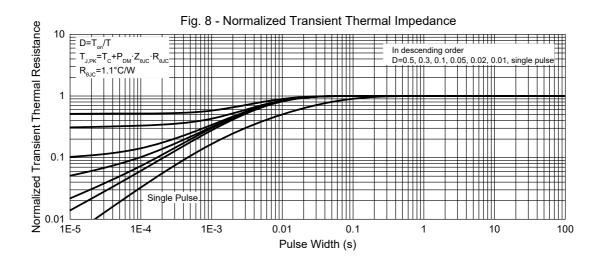






#### **Curve Characteristics**





Rev.3-2-04092022 4/5 MCCSEMI.COM



#### **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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