

Features

- · AEC-Q101 Qualification
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)

Maximum Ratings

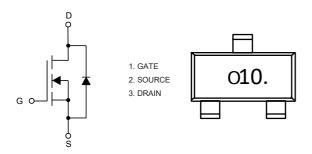
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 105°C/W Junction to Ambient^(Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Volltage	V_{GS}	±20	V
Drain Current	I _D	3.0	Α
Pulsed Drain Current ^(Note 3)	I _{DM}	10	Α
Total Power Dissipation	P_D	1.2	W

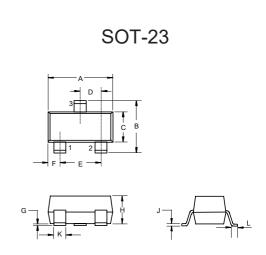
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

- 2. Device Mounted on FR-4 PCB, 1 inch 2 pad of 2oz copper.
- 3. Repetitive Rating : Pulse Width Limited by Junction Temperature.

Internal Structure and Marking Code

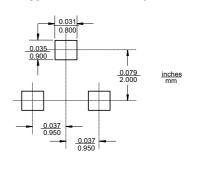


N-Channel Enhancement Mode Field Effect Transistor



DIMENSIONS						
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.110	0.120	2.80	3.04		
В	0.083	0.104	2.10	2.64		
С	0.047	0.055	1.20	1.40		
D	0.034	0.041	0.85	1.05		
E	0.067	0.083	1.70	2.10		
F	0.018	0.024	0.45	0.60		
G	0.0004	0.006	0.01	0.15		
Н	0.035	0.043	0.90	1.10		
J	0.003	0.007	0.08	0.18		
K	0.014	0.020	0.35	0.51		
L	0.007	0.020	0.20	0.50		

Suggested Solder Pad Layout





Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics	·					
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V
Gate-Source Leakage Current		V _{DS} =0V, V _{GS} =±20V			±100	nA
	I _{GSS}	V _{DS} =0V, V _{GS} =±10V			±50	IIA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA
Gate-Threshold Voltage ^(Note4)	V _{GS(th)}	$V_{DS}=V_{GS}, I_{D}=250\mu A$ 0.9			2.0	V
Drain-Source On-Resistance ^(Note4)	В	V _{GS} =10V, I _D =3A			100	mΩ
	$R_{DS(on)}$	V _{GS} =4.5V, I _D =2A			120	mΩ
Diode Forward Voltage ^(Note4)	V _{SD}	V _{GS} =0V, I _S =3A			1.2	V
Dynamic Characteristics (Note5,6)						
Input Capacitance	C _{iss}			409		pF
Output Capacitance	C _{oss}	V _{DS} =10V,V _{GS} =0V,f=1MHz		50		
Reverse Transfer Capacitance	C _{rss}			41		
Total Gate Charge	Qg			10.27		
Gate-Source Charge	Q_{gs}	V_{DS} =30V, V_{GS} =10V, I_{D} =3A		1.65		- nC
Gate-Drain Charge	Q_{gd}			2.11		
Reverse Recovery Charge	Q _{rr}	I _{SD} =3A, di/dt=100A/us		6.99		
Reverse Recovery Time	T _{rr}	ISD-OM, dirat-100Ards		32.6		
Turn-On Delay Time	t _{d(on)}			3.6		
Turn-On Rise Time	t _r	V_{GS} =10V, V_{DS} =30V,		17.6		ns
Turn-Off Delay Time	t _{d(off)}	$R_L=20\Omega,R_G=3\Omega$		13		
Turn-Off Fall Time	t _f			23		

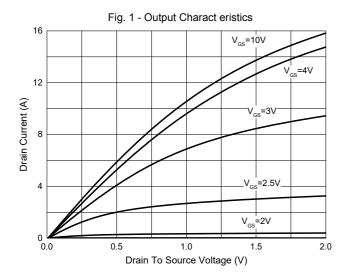
Note 4. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤ 2%.

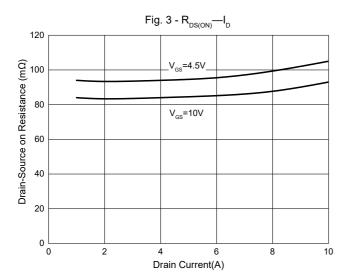
^{5.} Switching characteristics are independent of operating junction temperature.

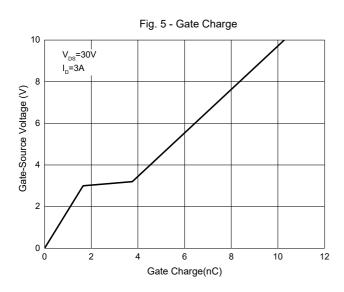
^{6.} Guaranteed by Design, Not Subject to Production Testing.

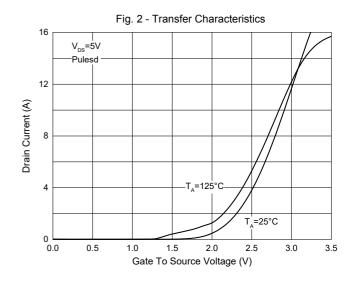


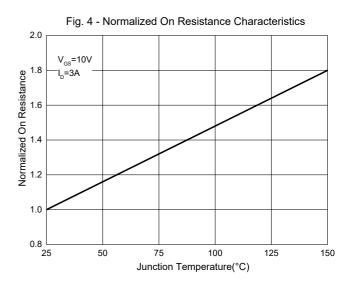
Curve Characteristics

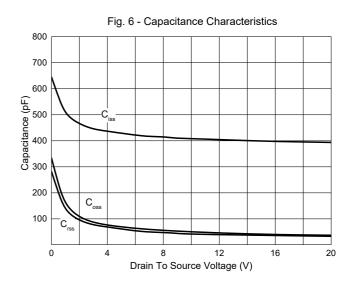














Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 3Kpcs/Reel	

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