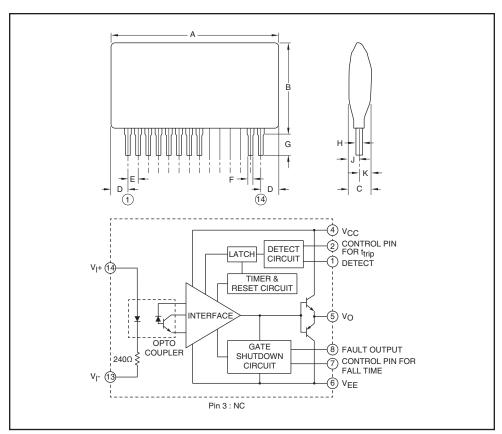


Powerex, Inc., 173 Pavilion Lane, Youngwood, Pennsylvania 15697 (724) 925-7272 www.pwrx.com

IGBT Gate Driver



Circuit Diagram

Dimensions	Inches	Millimeters
Α	1.73 Max.	44.0 Max.
В	1.02 Max.	26.0 Max.
С	0.33 Max.	8.5 Max.
D	0.21 Max.	5.5 Max.
Е	0.1	2.54
F	0.02+0.006/-0.00	04 0.5+0.15/-0.1
G	0.17±0.06	4.5±1.5
Н	0.01+0.008/-0.00	04 0.25+0.2/-0.1
J	0.24 Max.	6.5 Max.
K	0.12 Max.	3.0 Max.

Description:

VLA546-01R is a hybrid integrated circuit designed for driving n-channel IGBT modules in any gate-amplifier application. This device is a fully isolated gate drive circuit with an optically isolated gate drive amplifier that provides an over-current protection function based on desaturation detection.

Features:

- ☐ Electrical Isolation Between
 Input and Output via an
 Opto-coupler (V_{iso} = 4000V_{rms}
 for 1 Minute)
- ☐ Two Supply Drive Topology
- ☐ Built in Short-circuit Protection with a Pin for Fault Output
- ☐ Variable Fall Time on Activity of Short-circuit Protection
- ☐ TTL Compatible Input Interface

Applications:

To Drive IGBT Modules for Inverter or AC Servo Systems Application.

Recommended IGBT Modules:

V_{CES} = 600V Series up to 600A Class

V_{CES} = 1200V Series up to 400A Class

V_{CES} = 1700V Series up to 400A Class



VLA546-01R **IGBT Gate Driver**

Absolute Maximum Ratings, $T_a = 25^{\circ}\text{C}$ unless otherwise specified

Characteristics	Symbol	Rating	Units
Supply Voltage (DC)	V _{CC}	18	V
Supply Voltage (DC)	V _{EE}	-15	V
Input Signal Voltage (Applied Between Pin 13 and Pin 14, 50% Duty Cycle, Pulse Width 1ms)	VI	-1 ~ +7	V
Output Voltage (When Output Voltage is "H")	V _O	V _{CC}	V
Output Peak Current (Pulse Width 2µs)	I _{OHP}	-5	Α
	I _{OLP}	5	
Isolation Voltage (Sine Wave Voltage 60Hz, for 1 min.)	V _{iso}	4000	V _{rms}
Case Temperature	T _C	95	°C
Operating Temperature (No Condensation Allowable)	T _{opr}	-20 ~ +85	°C
Storage Temperature (No Condensation Allowable)	T _{stg}	-40 ~ +100 ^{*1}	°C
Fault Output Current (Applied at Pin 8)	I _{FO}	20	mA
Input Voltage at Pin 1	V _{R1}	50	V

Electrical Characteristics, $\rm T_a$ = 25°C, $\rm V_{CC}$ = 15V, $\rm V_{EE}$ = -10V, $\rm R_G$ = 3.0 Ω

Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{CC}	Recommended Range	14	15	17	V
Supply Voltage	V _{EE}	Recommended Range	-7	_	-12	V
Pull-up Voltage on Primary Side	V _{IN}	Recommended Range	4.75	5	5.25	V
"H" Input Signal Current	lіН	Recommended Range	10	13	16	mA
Switching Frequency	f	Recommended Range	_	_	20	kHz
Gate Resistance	R _G	Recommended Range	2	_	_	Ω
"H" Input Signal Current	I _H	V _{IN} = 5V, HC04 Drive	_	13	_	mA
"H" Output Voltage	V _{OH}	_	13	14	_	V
"L" Output Voltage	V _{OL}	_	-8	-9	_	V
"L-H" Propagation Time	t _{PLH}	I _{IH} = 13mA	0.2	0.4	1	μs
"L-H" Rise Time	t _r	I _{IH} = 13mA	_	0.3	1	μs
"H-L" Propagation Time	t _{PHL}	I _{IH} = 13mA	0.2	0.4	1	μs
"H-L" Fall Time	t _f	I _{IH} = 13mA	_	0.3	1	μs
Timer	t _{timer}	Between Start and Cancel	1	_	2	ms
		(Under Input Sign "L")				
Fault Output Current	I _{FO}	Applied at Pin 8, $R = 4.7k\Omega$	_	5	_	mA
Controlled Time Detect Short-Circuit 1	t _{trip1}	Pin 1: 15V or more, Pin 2: Open	_	2.6	_	μs
Controlled Time Detect Short-Circuit 2*2	t _{trip2}	Pin 1: 15V or more, Pins 2-4: 10pF	_	3	_	μs
	•	(Connective Capacitance)				
SC Detect Voltage	V _{SC}	Collector Voltage of IGBT	15	_	_	V

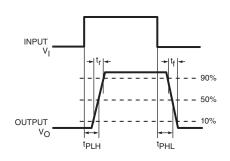
^{*1} Differs from H/C condition.
*2 The length of the wiring capacitor from Pin 2 to Pin 4 should be less than 5cm.



VLA546-01R IGBT Gate Driver

Definition of Characteristics

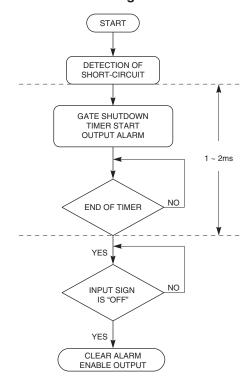
SWITCHING OPERATION



Operation of Protection Circuit

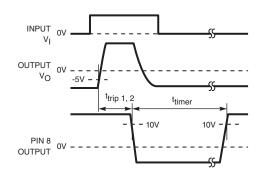
- In the case where the gate voltage is "H" and the collector voltage is high, the hybrid IC will recognize a short-circuit condition and immediately reduce the gate voltage. Additionally, it will output an error signal ("L") which indicates that the protection circuit is operating at the same time from Pin 8.
- The protection circuit resets if the input signal is "OFF" when the premised 1~2msec passed. ("OFF" period needs 10μm or more.)
- When the output rises, the controlled time detect short-circuit (typically 2.6µs) is set up so that the on-time of the IGBT can be secured properly. It is possible to adjust this time by connecting a capacitor (C_{trip}) between Pin 2 and Pin 4.

Operation Flow on Detecting Short Circuit



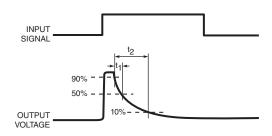
NOTE: Output voltage with protection circuit operating is about -IV $_{\mbox{EEI}}$ + 2V

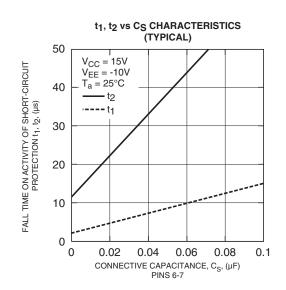
SHORT CIRCUIT PROTECTION



Adjustment of Output Fall Time

When the protection circuit is operating, the speed of reverse bias can be adjusted by connecting a capacitor (Cs) between Pin 6 and Pin 7.



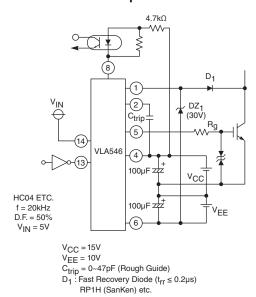


3



VLA546-01R **IGBT Gate Driver**

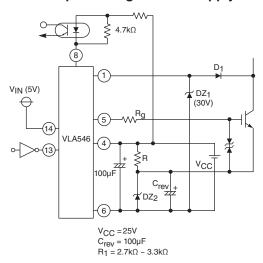
Application Circuit Example



Precaution

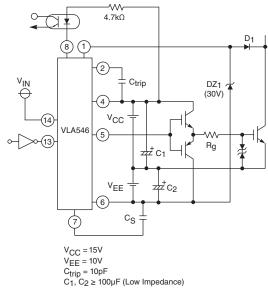
- 1. Voltage compensation capacitors are expected to be located as close as possible to the hybrid IC.
- D₁ requires approximately the same voltage rating as the power module.
- 3. If reverse recovery time of D₁ is long, high voltage is applied to Pin1. In this case, a zener diode should be inserted between Pin 1 and Pin 6 for necessary protection, as shown above.
- 4. If Pin 2 is operating, $C_{\mbox{trip}}$ is expected to be wired as close as possible to Pin 2 and Pin 4 and be less than 5cm.

Application Example of Single Power Supply



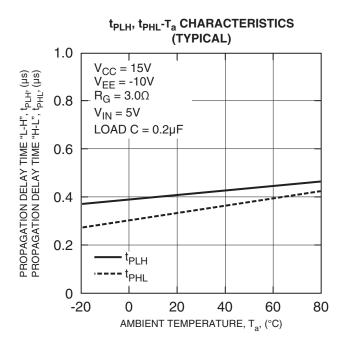
NOTE: Do not apply the "ON" signal until $t = R_1 \times C_{rev(s)}$ has passed after supplying power to the IC.

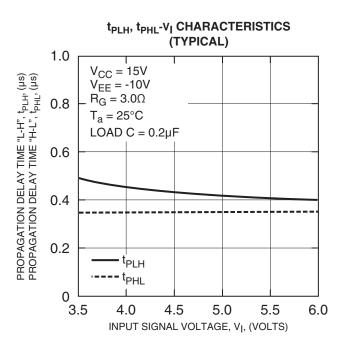
Application Example of High Power Supply

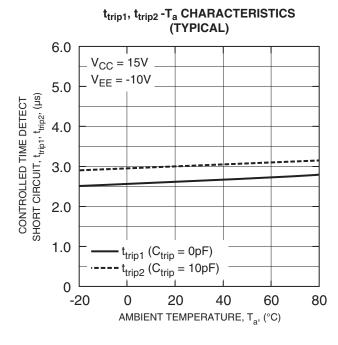


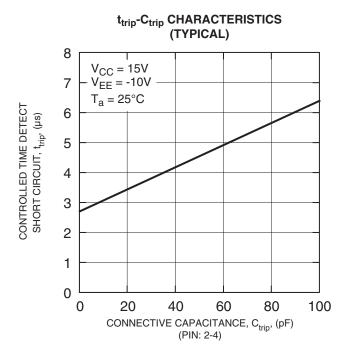


VLA546-01R IGBT Gate Driver





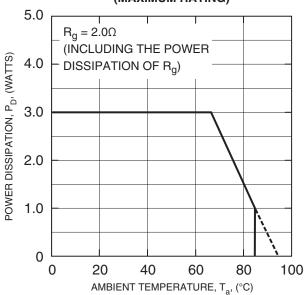






VLA546-01R IGBT Gate Driver

POWER DISSIPATION - AMBIENT TEMPERATURE CHARACTERISTICS (MAXIMUM RATING)



CONSUMPTION CURRENT - SUPPLY VOLTAGE (PIN: 4-6) INPUT SIGNAL "L" (TYPICAL)

