

Process Change Notification Announcement

Dear customer

We would like to inform that there would be some changes on these following detectors:

[Starting date: 01.12.2020](#)

Product name:

1. IRD PYQ1548/7660.

What would be change and what is the reason:

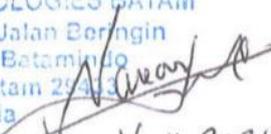
As a part of continuous improvement and product platform performance review, Excelitas Technologies perform change of ADC PIR Offset specification for PYQ1548 as follow:

PIR ADC Offset [Counts]			
Before		After	
Min	Max	Min	Max
7000	9000	6400	9000

The special element configuration of PYQ1548 leads to a wider distribution of the PIR ADC Offset value. Some of the ADC offset value are out from current minimum specification. Since the offset value does not influence the sensor performance (responsivity, noise, etc.), parts below the minimum specification of 7000 counts are considered as good parts. Referring to ADC offset distribution data, we set new Minimum limit at 6400 counts. Excelitas Technologies warrants the changing will not affect product performance.

We appreciate your understanding and cooperation.
Please feel free to contact us if you have any questions and concerns.

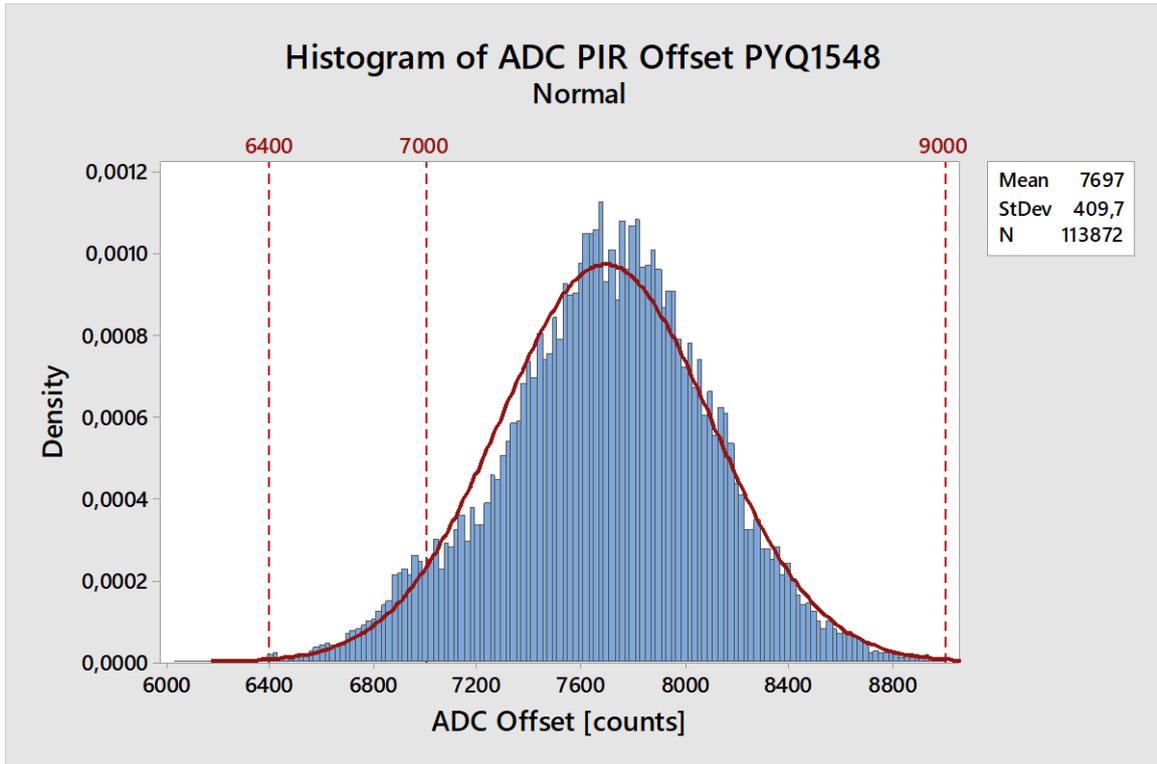
Best regards,

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Nanan Kurnia 
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(QA Senior Manager/ Excelitas Technologies Batam)

Appendix

Appendix 1, ADC PIR Offset Histogram



Appendix 2, Responsivity data validation.

