



DMP2012SN

Product Summary

BV _{DSS}	Rds(on) max	ID TA = +25°C
2017	0.3Ω @ V _{GS} = -4.5V	-0.9A
-20V	0.5Ω @ V _{GS} = -2.5V	-0.7A

Description

This MOSFET has been designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- **DC-DC** Converters
- **Power Management Functions**

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance •
- Fast Switching Speed •
- **ESD** Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/

P-CHANNEL ENHANCEMENT MODE MOSFET

Mechanical Data

- Package: SC59
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram
- Weight: 0.014 grams (Approximate)



Source Equivalent Circuit

Drain

Ordering Information (Note 4)

ESD PROTECTED

Part Number	Compliance	Pookago	Packing		
Fart Nulliber	Compliance	Package	Qty.	Carrier	
DMP2012SN-7	Standard	SC59	3000	Tape & Reel	

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

\Box	
PS1	ΥM

SC59

Top View

PS1 = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: I = 2021)

M = Month (ex: 9 = September)

Data Cada Kay

Notes:

Date Code Key												
Year	2006		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Т		-	J	K	L	М	Ν	0	Р	R	S
				-		-		-	-			_
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	Vdss	-20	V
Gate-Source Voltage	Vgss	±12	V
Drain Current (Note 5) Steady State	lo	-0.9	A
Pulsed Drain Current (Note 6)	I _{DM}	-2.8	A

Thermal Characteristics

<u></u>			
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	500	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	250	°C/W
Operating and Storage Temperature Range	Tj, Tstg	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			71			l .	
Drain-Source Breakdown Voltage	BVDSS	-20		_	V	Vgs = 0V, Id = -250µA	
Zero Gate Voltage Drain Current	IDSS	_		-10	μA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Body Leakage	lgss	_		±10	μA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	VGS(TH)	-0.5		-1.2	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	
Static Drain-Source On-Resistance	Proven		0.23	0.3	Ω	Vgs = -4.5V, ID = -0.4A	
Static Drain-Source On-Resistance	R _{DS(ON)}		0.37	0.5	12	$V_{GS} = -2.5V, I_D = -0.4A$	
Forward Transfer Admittance	Y _{fs}	_	1.5		S	$V_{DS} = -10V, I_{D} = -0.4A$	
Diode Forward Voltage (Note 7)	V _{SD}	_	-0.8	-1.1	V	$V_{GS} = 0V, I_{S} = -0.7A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	Ciss	_	178.5	—	pF		
Output Capacitance	Coss	_	26.3	_	pF	$V_{DS} = -10V, V_{GS} = 0V$	
Reverse Transfer Capacitance	Crss	_	18.8		pF	f = 1.0MHz	
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	td(on)	_	10.4		ns		
Turn-Off Delay Time	tD(OFF)	_	175	_	ns	V _{DD} = -10V, I _D = -0.4A,	
Turn-On Rise Time	tr		22.3		ns	$V_{GS} = -5.0V, R_{GEN} = 50\Omega$	
Turn-Off Fall Time	tr	_	64	—	ns		

Notes: 5. Device mounted on FR-4 PCB.

6. Pulse width $\leq 10\mu$ S, Duty Cycle $\leq 1\%$. 7. Short duration pulse test used to minimize self-heating effect.













Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



SC59					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
c	2.70	3.00	2.80		
D	-	-	0.95		
G	-	-	1.90		
H	2.90	3.10	3.00		
J	0.013	0.10	0.05		
К	1.00	1.30	1.10		
L	0.35	0.55	0.40		
Μ	0.10	0.20	0.15		
Ν	0.70	0.80	0.75		
α	0°	8°	-		
	Dimens	ions in	mm		

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Y	1.0
С	2.4
E	1.35

SC59

SC59



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