1. General description

NPN/NPN general-purpose transistor pair in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- · Low collector capacitance
- Low collector-emitter saturation voltage
- · Closely matched current gain
- · Reduces number of components and board space
- No mutual interference between the transistors

3. Applications

· General-purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
V _{CEO}	collector-emitter voltage	open base	-	-	65	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	200	300	450	



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	Пе Пе Пи	C1 B2 E2
2	B1	base TR1	6 5 4	
3	C2	collector TR2		(TR1) TR2)
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1	TSSOP6 (SOT363)	sym020

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BC846BS		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BC846BS	%E5

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or					
V _{CBO}	collector-base voltage	open emitter		-	80	V
V _{CEO}	collector-emitter voltage	open base		-	65	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device			·			
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

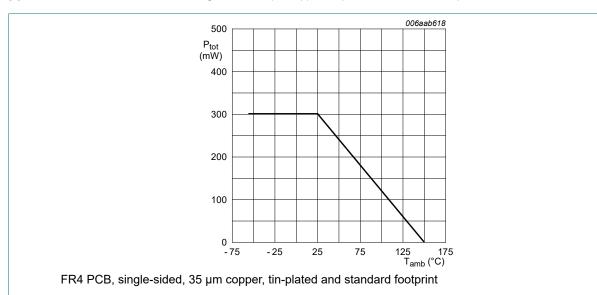


Fig. 1. Per device: Power derating curve

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor		,				
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	230	K/W
Per device					-	1	
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

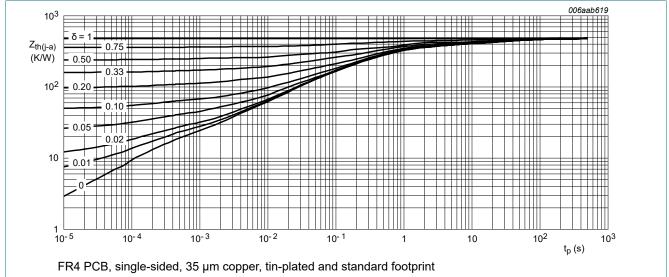


Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

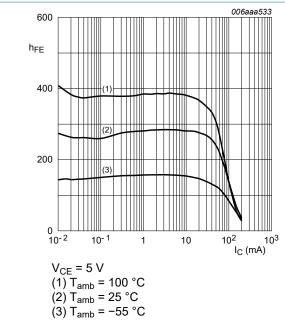
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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	or					
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C	80	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	65	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I _C = 0 A; I _E = 100 μA; T _{amb} = 25 °C	6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE} DC currer	DC current gain	V _{CE} = 5 V; I _C = 10 μA; T _{amb} = 25 °C	-	280	-	
		V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	200	300	450	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	55	100	mV
		I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C	-	200	300	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	755	850	mV
	voltage	I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C	-	1000	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	580	650	700	mV
		V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C	-	-	770	mV
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	-	1.9	-	pF
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	11	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz
NF	noise figure	$V_{CE} = 5 \text{ V}; I_{C} = 0.2 \text{ mA}; R_{S} = 2 \text{ k}\Omega;$ $10 \text{ Hz} \le f \le 15700 \text{ Hz}; T_{amb} = 25 \text{ °C}$	-	1.9	-	dB
		$V_{CE} = 5 \text{ V}; I_{C} = 0.2 \text{ mA}; R_{S} = 2 \text{ k}\Omega;$ f = 1 kHz; B = 200 Hz; $T_{amb} = 25 \text{ °C}$	-	3.1	-	dB

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Per transistor: DC current gain as a function of Fig. 3. collector current; typical values

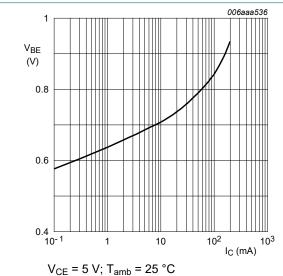
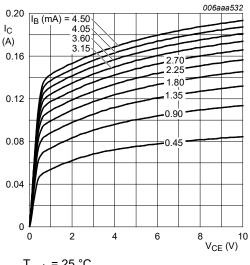
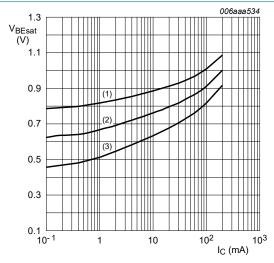


Fig. 5. Per transistor: Base-emitter voltage as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 4. Per transistor: Collector current as a function of collector-emitter voltage; typical values



 $I_{\rm C}/I_{\rm B} = 20$

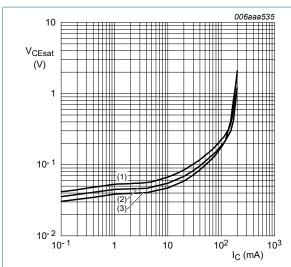
(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 100 \, ^{\circ}C$

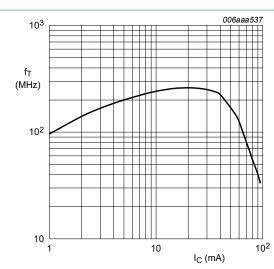
Fig. 6. Per transistor: Base-emitter saturation voltage as a function of collector current; typical values

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I_C/I_B = 20 (1) T_{amb} = 100 °C (2) T_{amb} = 25 °C (3) T_{amb} = -55 °C

Per transistor: Collector-emitter saturation Fig. 7. voltage as a function of collector current; typical values



 V_{CE} = 5 V; T_{amb} = 25 °C

Per transistor: Transition frequency as a Fig. 8. function of collector current; typical values

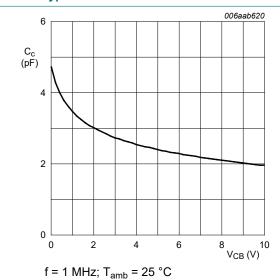


Fig. 9. Per transistor: Collector capacitance as a function of collector-base voltage; typical values

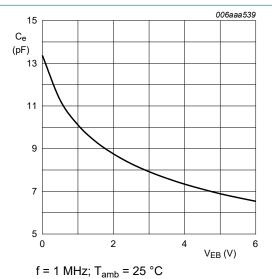
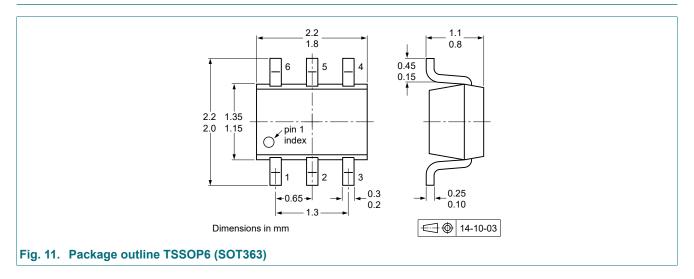


Fig. 10. Per transistor: Emitter capacitance as a function of emitter-base voltage; typical values

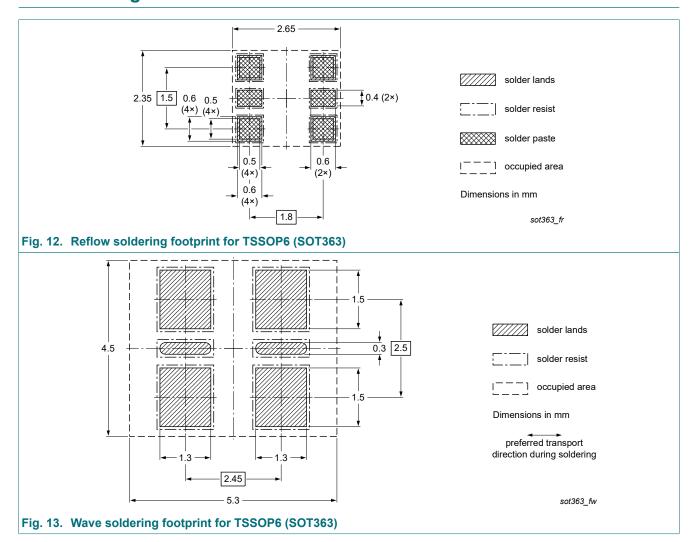
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11. Package outline



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12. Soldering



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13. Revision history

Table 8. Revision history

Table of Novicion motory								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
BC846BS v.2	20220701	Product data sheet	-	BC846BS v.1				
Modification:	 Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s). Packing information removed. 							
BC846BS v.1	20090824	Product data sheet	-	-				

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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