

SAW Components

Data Sheet B3571





SAW Components	B3571
Low-loss Filter	868,60 MHz

Data Sheet

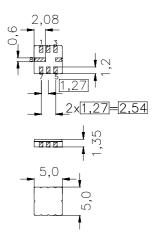
Features

- RF low-loss filter for remote control receivers
- Package for **S**urface **M**ounted **T**echnology (**SMT**)

Terminals

■ Ni, gold plated

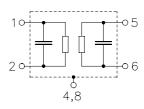
Ceramic package QCC8C



typ. dimensions in mm, approx. weight 0,1 g

Pin configuration

- 2 Input
- 1,3 Input Ground
- 6 Output
- 5,7 Output Ground
- 4,8 Case Ground



Туре	Ordering code	Marking and package according to	Packing according to
B3571	B39871-B3571-U310	C61157-A7-A56	F61074-V8070-Z000

Electrostactic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T_{A}	-45/+90	°C	
Storage temperature range	$T_{\rm stg}$	-45/+90	°C	
DC voltage	$V_{\rm DC}$	0	V	
Source power	P_S	0	dBm	source impedance 50 Ω



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Characteristics

Reference temperature:

 $T_{\rm A} = 25~^{\circ}{\rm C}$ $Z_{\rm S} = 50~\Omega$ and matching network $Z_{\rm L} = 50~\Omega$ and matching network Terminating source impedance: Terminating load impedance:

		min.	typ.	max.	
Center frequency	f_C	_	868,69	_	MHz
(center frequency between 3 dB points)					
Minimum insertion attenuation	α_{min}				
868,00 869,38 MHz		_	3,1	4,6	dB
Pass band (relative to α_{min})					
868,00 869,38 MHz		_	1,5	3,0	dB
867,92 869,46 MHz		_	2,0	6,0	dB
Relative attenuation (relative to α_{min})	$lpha_{\text{rel}}$				
10,00 700,00 MHz		50	55		dB
700,00 830,00 MHz		33	38		dB
830,00 858,00 MHz		30	35		dB
858,00 866,40 MHz		20	25		dB
871,00 880,00 MHz		17	22		dB
880,00 910,00 MHz		30	35		dB
910,001000,00 MHz		33	38		dB
Impedance for pass band matching ²⁾					
Input: $Z_{IN} = R_{IN} \parallel C_{IN}$		<u> </u>	226 2,30		$\Omega \parallel pF$
Output: $Z_{OUT} = R_{OUT} C_{OUT}$		_	222 2,20	_	Ω pF
Temperature coefficient of frequency 1)	TC_{f}	_	-0,03	_	ppm/K ²
Frequency inversion point	T_0	_	25		°C

¹⁾Temperature dependence of f_C : $f_C(T_A) = f_C(T_0)$ (1 + $TC_f(T_A - T_0)^2$)

The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details, we refer to EPCOS application note #4.

²⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After the SAW filter is removed and input impedance into the input matching / output matching network is calculated.



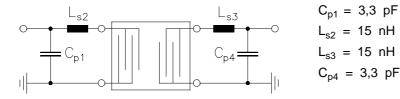
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> $C_{p1} = 3.3 pF$ $L_{s2} = 15 \text{ nH}$

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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)

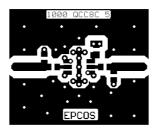


Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout.

Grounding pins for input transducer are pin 1,3 and for output transducer 5,7. Close to those pins via holes (through holes) should be placed to achieve a low impedance path to system ground. If a grounding plane at the top side of the PCB is present, the grounding plane can be connected to pin 1,3,5,7 at the top side too.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here.



Optimised PCB layout for SAW filters in QCC8C package, pinning 2,6 (top side, scale 1:1)

The bottom side is a copper plane (system ground area).

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

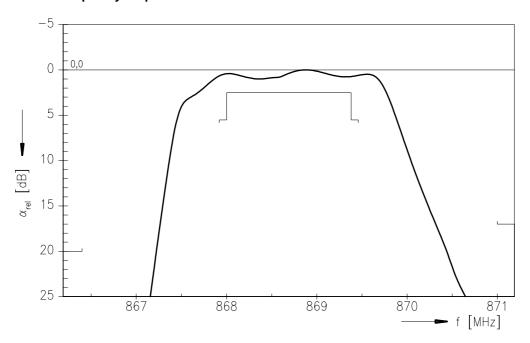


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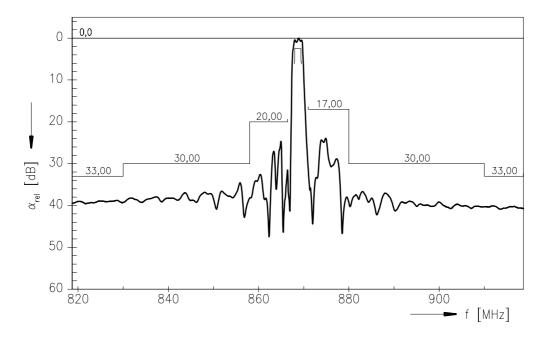
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Normalized frequency response



Normalized frequency response (wideband)





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