											T
<b>PCN Number:</b> 20160624002A		24002A				F	PCN Date:		07/14/2016		
Title: Qualify New Assembly Material set for Selected Device(s)											
Customer Contact: PCN Manager Dept: Quality Services											
Propo	osed 1 <sup>st</sup> Shi	p Date:	09/27	/2016			Estimated Ava		mple pility:		provided at ble request
Chan	ge Type:									-	
	ssembly Site	9		Des	sign				Wafer Bump Site		
⊠ A	ssembly Pro	cess		Dat	ta S	heet			Wafer Bump Material		
⊠ A	ssembly Ma	terials		Par	Part number change			Wafer Bump Process			
	1echanical S	pecificatio	n	Tes	Test Site			Wafe	r Fab S	Site	
P	acking/Ship	oing/Labe	ling	Tes	t Pr	ocess			Wafe	ifer Fab Materials	
									Wafer Fab Process		Process
				P	CN	l Detail	S				
Desci	ription of C	hange:									
table. for an Texas as an	Revision A is to update the description of change to provide correction on the material differences table. Mount compound change for Group 1 devices will only be for the Controller Die. We apologize for any inconvenience this may have caused.  Texas Instruments is pleased to announce the qualification of new assembly material set to add Cu as an additional bond wire option for devices listed in "Product affected" section below. Devices will remain in current assembly facility and piece part changes as follows:										
	p 1 Device:	,	,		, p						
	Mate	rial		Curre	nt		Dr	ono	sed		1
	Wire	1101					il Cu		l		
	Mount com	pound		0150 1111						l	
	(Controller die) 422083		38 (Sol	der	paste)	42071	123	(Epoxy	)	l	
Grou	p 2 Device:										
	Mate	rial	Cur	rent		Propose	d				
	Wire	-	0.96 ı	nil Au		).80 mil C					
Reas	on for Chan	ge:					<u>.</u>				
Continuity of supply.  1) To align with world technology trends and use wiring with enhanced mechanical and electrical properties  2) Maximize flexibility within our Assembly/Test production sites.  3) Cu is easier to obtain and stock											
Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):											
None											
Antic	ipated imp	act on M	aterial D	eclarat	tion						
Material Declaration produce release			duction ( ase. Up	al Declarations or Product Content reports are driven from ction data and will be available following the production e. Upon production release the revised reports can be ed from the TI ECO website.				roduction			
Chan	Changes to product identification resulting from this PCN:										
None											

Group 1 Product Affected:				
HPA01110DQPR	TPS53353DQPT	TPS544B25RVFT	TPS56121DQPR	
HPA01111DQPR	TPS53355ADQPR	TPS544C24RVFR	TPS56121DQPT	
SN1109022DQPR	TPS53355ADQPT	TPS544C24RVFT	TPS56221ADQPR	
TPS53318DQPR	TPS53355DQPR	TPS544C25RVFR	TPS56221BDQPR	
TPS53318DQPT	TPS53355DQPT	TPS544C25RVFT	TPS56221BDQPT	
TPS53319DQPR	TPS544B24RVFR	TPS56121ADQPR	TPS56221DQPR	
TPS53319DQPT	TPS544B24RVFT	TPS56121BDQPR	TPS56221DQPT	
TPS53353DQPR	TPS544B25RVFR	TPS56121BDQPT		

## **Group 2 Product Affected:**

AM1705DPTPD4

## **Group 1 Qualification Report**

# Cu Wire Qualification on Std Al (0.6um) bond pads for Clip QFN Devices in Tl Clark

Approve Date 10-June-2016

### **Product Attributes**

1 1 dad 1 / tti i dat 0					
Attributes	Qual Device: CSD95372AQ5M POWER STAGE	Qual Device: TPS544C24RVFR TOP AVATAR			
Assembly Site	CLARK AT	CLARK AT			
Package Family	LSON-CLIP	LQFN-CLIP			
Flammability Rating	UL 94 V-0	UL 94 V-0			
Wafer Fab Supplier	CFAB, MH8	CFAB, MH8			
Wafer Fab Process	LBC7, N35ULD11L1P1M0C1, N35ULD11L1P1M0C4	LBC7X, N35ULD09L1P1M0C1			

- QBS: Qual By Similarity
- Qual Device CSD95372AQ5M POWER STAGE is qualified at LEVEL2-260C
- Qual Device TPS544C24RVFR TOP AVATAR is qualified at LEVEL2-260C
- Device CSD95372AQ5M POWER STAGE contains multiple dies.
- Device TPS544C24RVFR TOP AVATAR contains multiple dies.

## **Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: CSD95372AQ5M POWER STAGE	Qual Device: TPS544C24RVFR TOP AVATAR
AC	**Autoclave 121C	121C, 2 ATM (96 Hours)	3/231/0	-
HAST	**Biased HAST	130C/85%RH/33.3 psia (96 Hours), Vddmax	3/231/0	-
HTSL	High Temp. Storage Bake	150C (1000 Hours)	3/231/0	-

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Туре	Test Name / Condition	Duration	Qual Device: CSD95372AQ5M POWER STAGE	Qual Device: TPS544C24RVFR TOP AVATAR
TC	**T/C -55C/125C	-55C/+125C (700 Cycles)	3/231/0	3/231/0

- Preconditioning was performed for Autoclave, THB/Biased HAST, Temperature Cycle, and HTSL, as applicable
- -- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles Quality and Environmental data is available at Tl's external Web site: http://www.ti.com/

#### Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

# **Group 2 Qualification Report**



**Digital Signal Processing** 

## Qualification Report PTP packages Copper wire qualification

Report compiled 8th August 2015

Qualification Information							
Qual Type:	Major Change	Affected	Wafer fab : TSMC-14				
Quai Type.	riajor change	Sites:	A/T : TI PHILIPPINES				
Affected business:	TI DSP products	Status:	Released				
Project Reference:							

The qualification was based on TI QSS 009-401 and Automotive Electronic Council AEC-Q100 rev G where test groups A and C were conducted to prove the change.

### CONSTRUCTION INFORMATION

Package Attributes: PTP Package					
Assembly Site	PHI	Mold Compound	4211649		
Bond Wire Composition	Cu	Package Designator	PTP		
Moisture Sensitivity Level	LEVEL3-260C	Package Size	24 x 24 mm		
Mount Compound	4208458	Pin Count	176		
Package Family	PowerPad LQFP	Leadframe Finish	NiPdAu		

Texas Instruments, Inc. PCN#20160624002A

Test Type	Duration / Stress /Test	Lots	Results	Fail	Qualification vehicle
AEC 0100: TEST GRO	DUPS A - ACCELERATED ENVI	RONMENT STRESS TESTS			
PC : Preconditioning	MSL3/ 260C	3 lots : All units prior to AC, TC, HTSL	0 / 864		TMS320C6743PTP
	CSAM inspection after Preconditioning	864 units	Validated no internal delamination observed		TMS320C6743PTP
UHAST: Unbiased HAST	110C/2ATM/264 hours	3 lots x 77 units	0/231 @ 264 hours		TMS320C6743PTP
Temperature Cycle	-65C/150C for 500 cycles	3 lots x 77 units	0/231 @ 500 cycles		TMS320C6743PTP
HTSL : High temp storage	150C for 1000 hours	1 lots x 45 units	0/231 @ 1000 hours		TMS320C6743PTP
5 F 1 T 1 5					
	sed Humidity testing is QBS to 5			s/ package i	
THB: Biased Humidity*	85C/85% RH 1000 hours	3 lots x 77 units	0/231 @ 1000 hours		52C1RFPT
*=devices were preco	nditioned to MSL3/260C prior to	THB			
AEC 0100: TEST GRO	OUPS C - PACKAGE INTEGRIT	TY TESTS			
WBP: Wire Bond pull	Cpk > 1.67	1 lot x 5 parts x 30 bonds	Pass		x021 QFP family data
WBS: Wire Bond Shear	Cpk > 1.67	1 lot x 5 parts x 30 bonds	Pass		x021 QFP family data

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