

Description

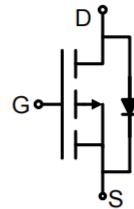
The G3035L uses advanced trench technology to provide excellent $R_{DS(ON)}$. This device is suitable for use as a load switch or in PWM applications.

General Features

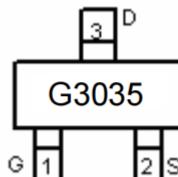
- V_{DSS} $R_{DS(ON)}$ @ -4.5V(Typ) $R_{DS(ON)}$ @ -10V(Typ) I_D
- -30V 60mΩ 48mΩ -4.1A
- High power and current handling capability
- RoHS Compliant
- Surface mount package

Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23-3L

Ordering Information

Part Number	Marking	Case	Packaging
G3035L	G3035	SOT-23-3L	3000pcs/Reel

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-4.1	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	-16	A
Maximum Power Dissipation	P_D	1.4	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	90	°C/W
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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V$ $I_D=-250\mu\text{A}$	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30V, V_{GS}=0V$	-	-	-0.3	μA

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250µA	-1	-1.5	-2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-2.1A	-	48	59	mΩ
		V _{GS} =-4.5V, I _D =-2.1A	-	60	75	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-4.1A	5.5	-	-	S
Dynamic Characteristics ^(Note4)						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, F=1.0MHz	-	650	-	PF
Output Capacitance	C _{oss}		-	105	-	PF
Reverse Transfer Capacitance	C _{rss}		-	65	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-15V, R _L =3.6Ω V _{GS} =-10V, R _{GEN} =3Ω	-	8.5	-	nS
Turn-on Rise Time	t _r		-	4.5	-	nS
Turn-Off Delay Time	t _{d(off)}		-	26	-	nS
Turn-Off Fall Time	t _f		-	12.5	-	nS
Total Gate Charge	Q _g	V _{DS} =-15V, I _D =-4A, V _{GS} =-10V	-	12.5	-	nC
Gate-Source Charge	Q _{gs}		-	2.8	-	nC
Gate-Drain Charge	Q _{gd}		-	2.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _s =-2A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

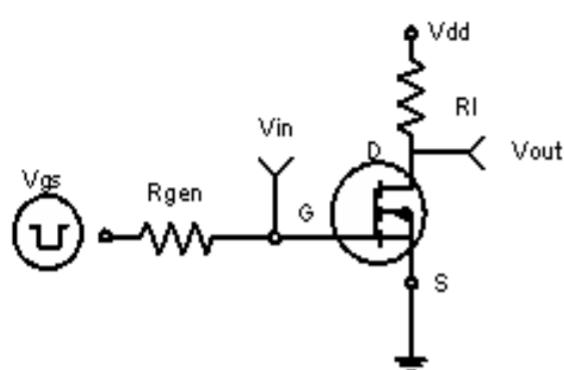


Figure 1:Switching Test Circuit

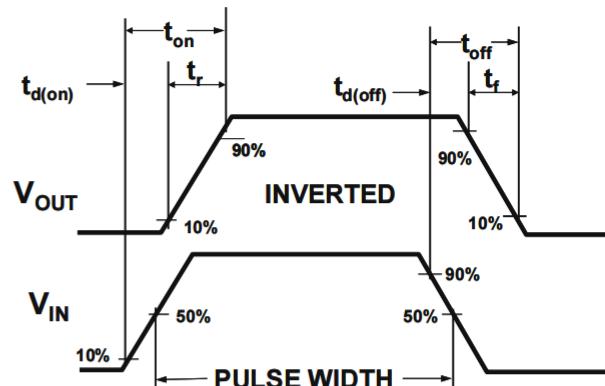


Figure 2:Switching Waveforms

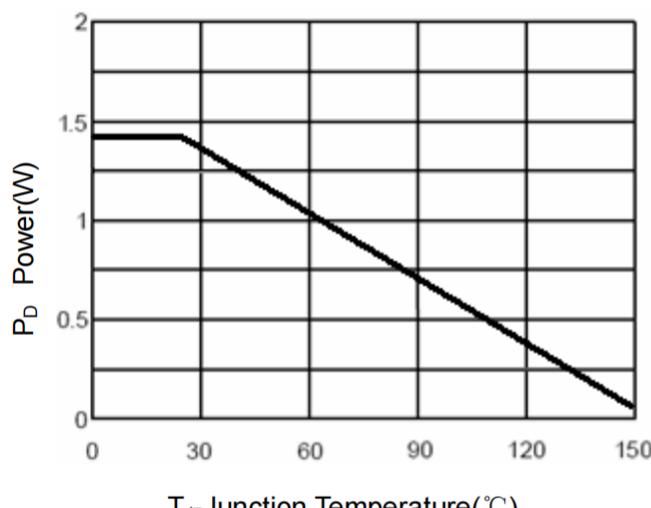


Figure 3 Power Dissipation

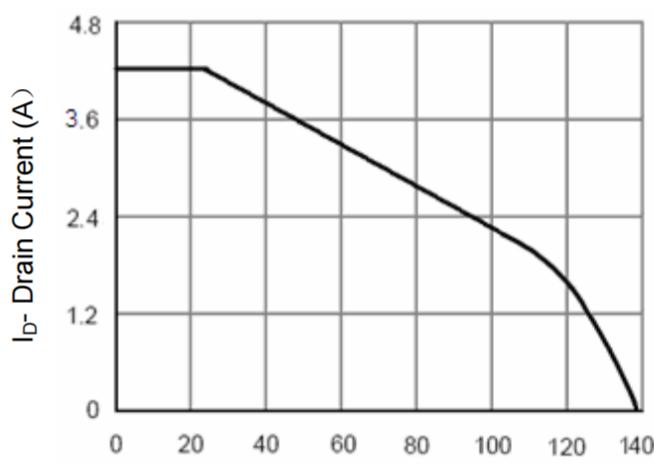


Figure 4 Drain Current

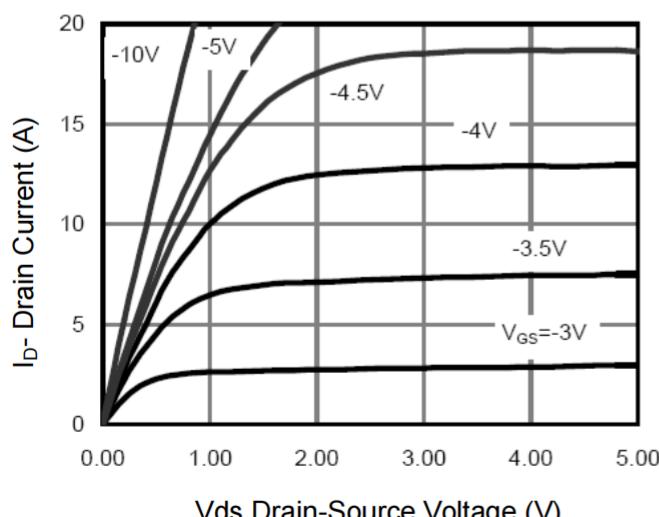


Figure 5 Output Characteristics

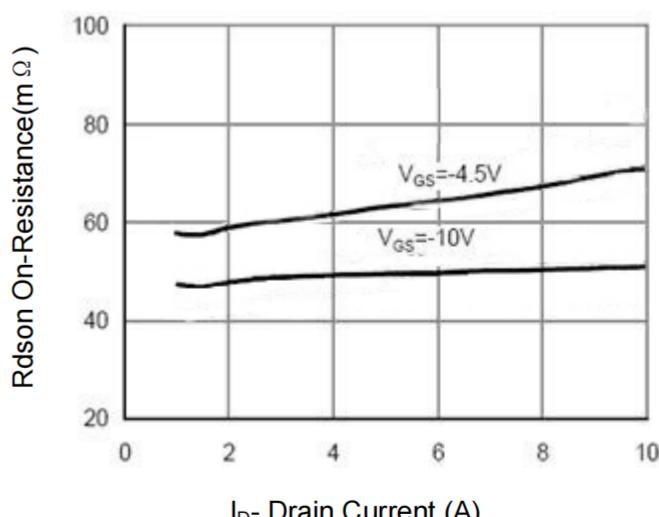


Figure 6 Drain-Source On-Resistance

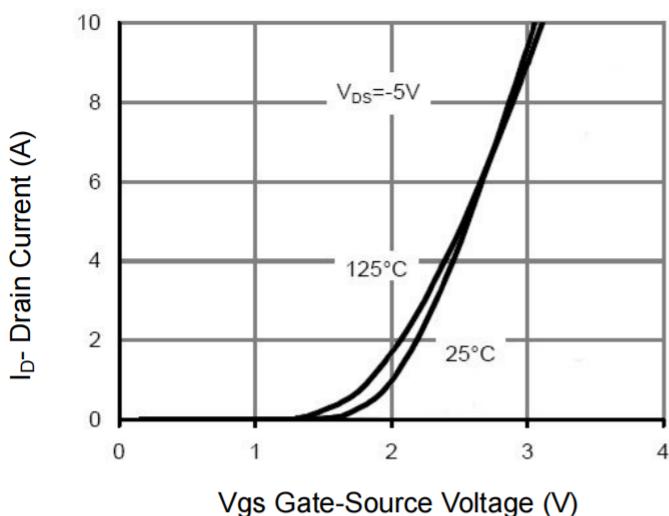


Figure 7 Transfer Characteristics

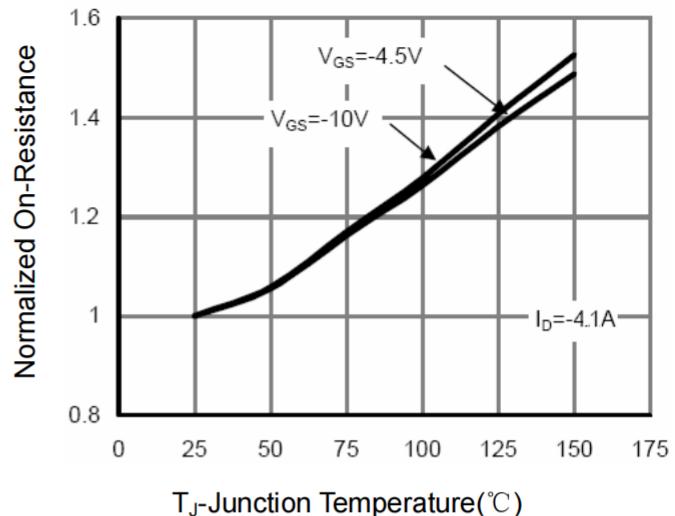


Figure 8 Drain-Source On-Resistance

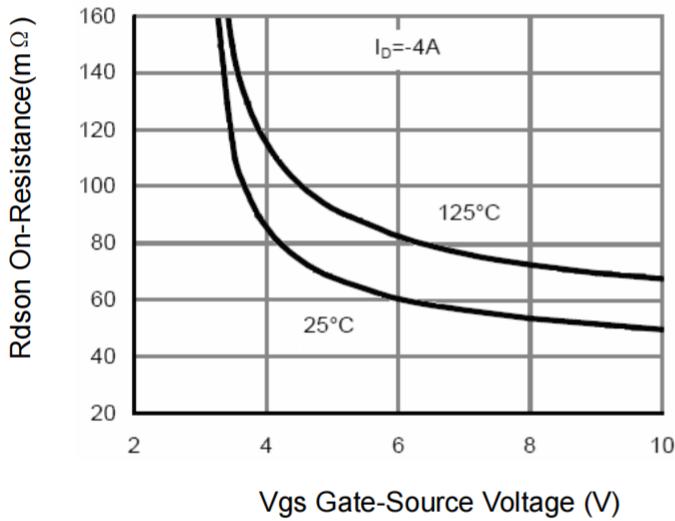


Figure 9 R_{DSON} vs V_{GS}

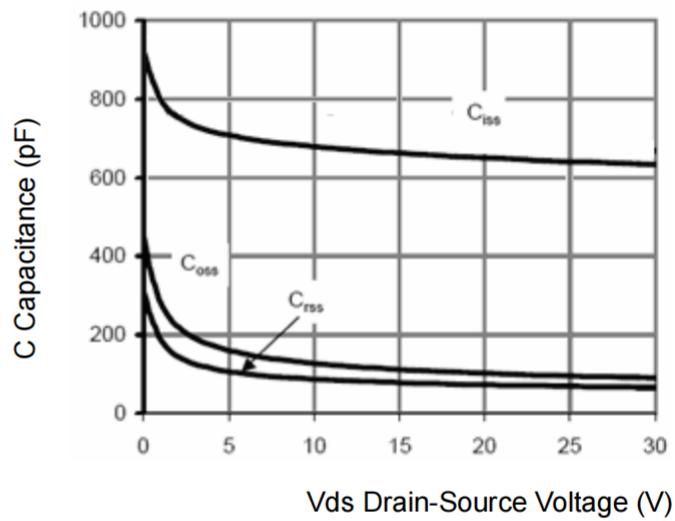


Figure 10 Capacitance vs V_{DS}

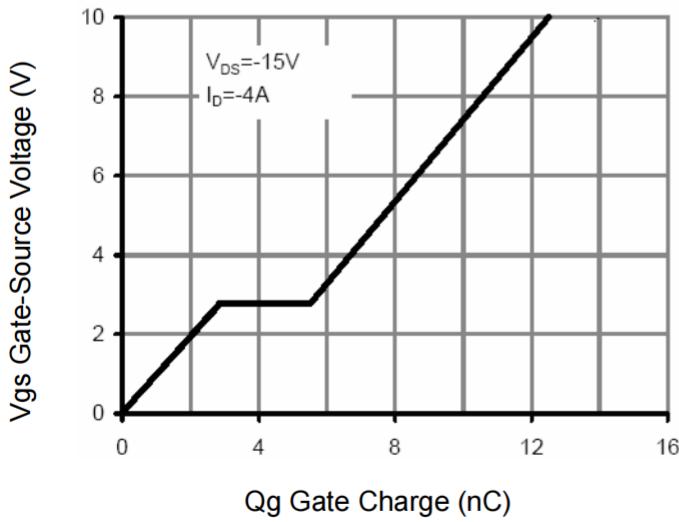


Figure 11 Gate Charge

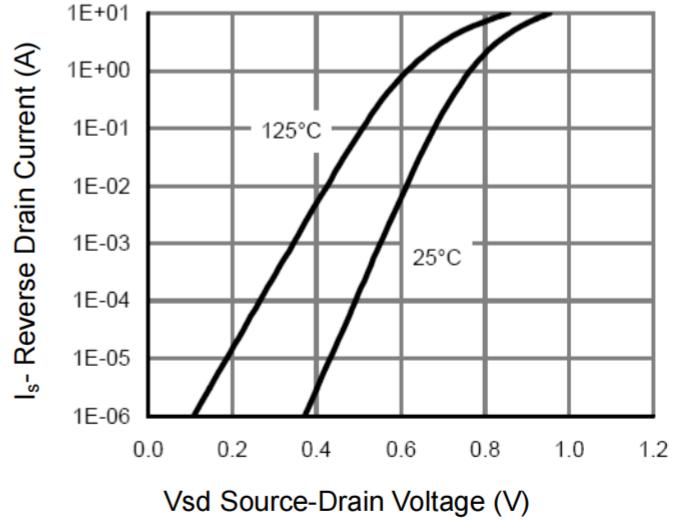


Figure 12 Source- Drain Diode Forward

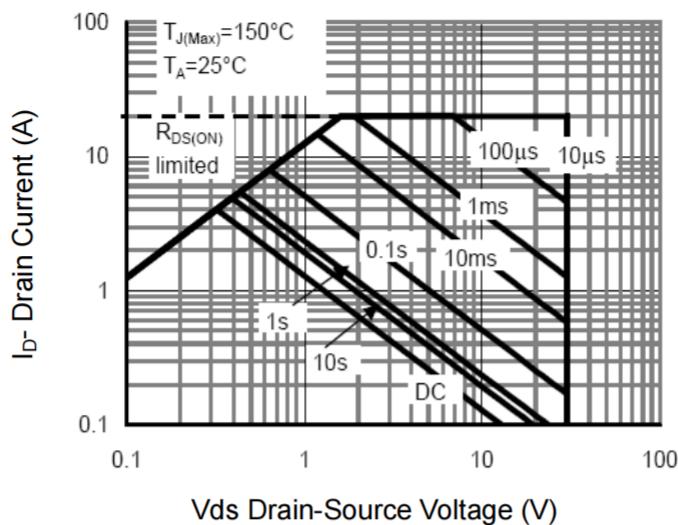


Figure 13 Safe Operation Area

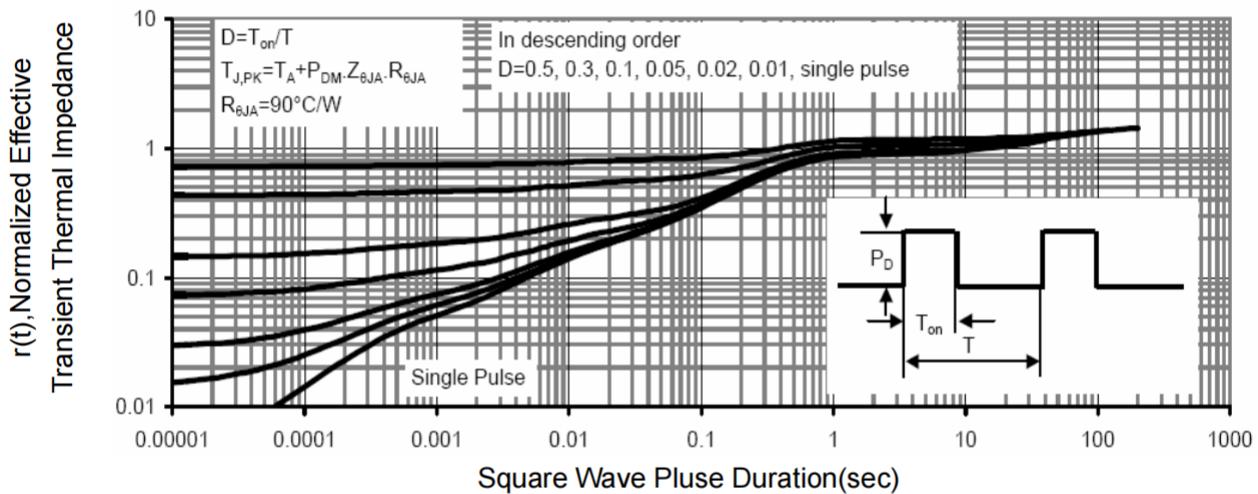
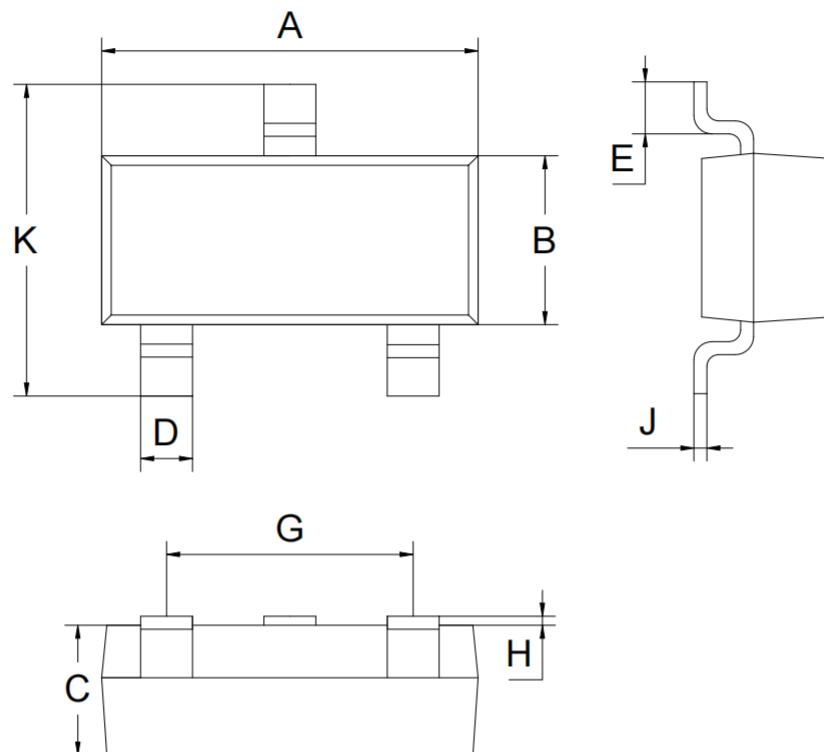


Figure 14 Normalized Maximum Transient Thermal Impedance

SOT-23-3L Package information



SOT-23-3L			
Dim	MIN	NOM	MAX
A	2.80	2.90	3.00
B	1.50	1.60	1.70
C	1.00	1.10	1.20
D	0.30	0.40	0.50
E	0.25	0.40	0.55
G	1.90		
H	0.00	-	0.10
J	0.047	0.127	0.207
K	2.60	2.80	3.00
All Dimensions in mm			