PQ5EV3/PQ5EV5/ PQ5EV7

■ Features

1. Low power-loss

(Dropout voltage: MAX.0.5V)

2. Package with exposed radiation fin (Equivalent to TO-220)

3. Large output current

3.5A:PQ5EV3, 5A:PQ5EV5, 7.5A:PQ5EV7

4. Variable output voltage (1.5V to 5V)

5. High-precision output type (Reference voltage precision:±1.0%)

6. Overcurrent, overheat protection functions

■ Applications

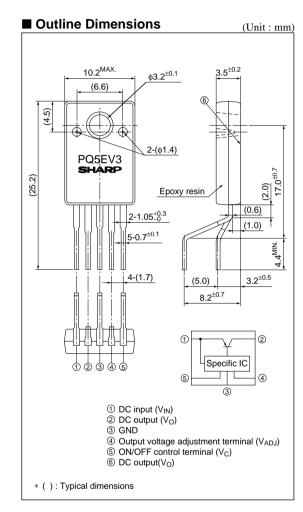
1. Personal computers

Power supplies for various electronic equipment such as AV or OA

■ Absolute N	(Ta=25°C)				
Parameter		Symbol	Rating	Unit	
*1 Input voltage		Vin	7	V	
Dropout voltage		V _{I-O}	4	V	
*1Output control voltage		Vc	7	V	
*1 Output adjustment terminal voltage		V _{ADJ}	5	V	
Output current	PQ5EV3	Io	3.5	A	
	PQ5EV5		5.0		
	PQ5EV7		7.5		
*2Power dissipation		PDI	1.6	W	
		P _{D2}	45	W	
*3 Junction temperature		Tj	150	°C	
Operating temperature		Topr	-20 to +80	°C	
Storage temperature		Tstg	-40 to +150	°C	
*4 Soldering temperature		Tsol	260	°C	

^{*1} All are open except GND and applicable terminals

Large Output Current Type Low Power-Loss Voltage Regulator



^{*2} P_{D1}:No heat sink, P_{D2}:With infinite heat sink

^{*3} Overheat protection may operate at the condition Ti:125°C to 150°C

^{*4} For 10s

■ Electrical Characteristics		(Unless otherwise specified, $V_{IN}=5V$, *5 , $V_{O}=3V$ ($R_{I}=2k\Omega$), $T_{a}=25^{\circ}C$)					
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Input voltage	Vin	_	2.35	_	7	V	
Output voltage	Vo	_	1.5	_	5	V	
Reference voltage	Vref	_	1.2276	1.24	1.2524	V	
Load line regulation	RegL	Io=5mA to rating	_	0.1	0.5	%	
Input line regulation	RegI	$V_{IN}=4$ to 7V, $Io=5mA$	_	0.05	0.1	%	
Reference voltage temperature coefficient	TcVref	T _j =0 to 125°C	_	±1	_	%	
Ripple Rejection	RR	Refer to Fig.2	60	70	_	dB	
Dropout voltage	V _{I-O}	*6	_	_	0.5	V	
*7 Output on control voltage	V _C (ON)	-	2	_	_	V	
Output on control current	Ic (on)	Vc=2.7V	_	_	20	μΑ	
Output off control voltage	V _{C (OFF)}	_	_	_	0.8	V	
Output off control current	Ic (OFF)	Vc=0.4V	_	-	-0.4	mA	
Non-operating dissipatiion current	Iq	Io=0A	_	10	15	mA	

^{*5} PQ5EV3:Io=1.75A, PQ5EV5:Io=2.5A, PQ5EV7:Io=3.75A

Fig.1 Standard Test Circuit

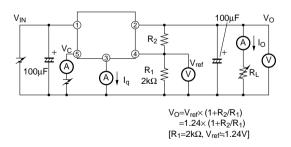
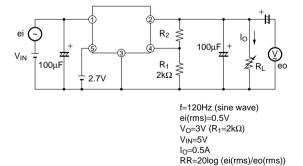


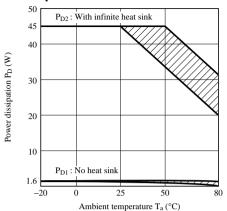
Fig.2 Test Circuit for Ripple Rejection



^{*6} PQ5EV3:IO=3.5A, PQ5EV5:IO=5A, PQ5EV7:IO=7.5A. Input voltage shall be the value when output voltage is 95% in comparison with the initial value

^{*7} In case of opening control terminal 5, output voltage turns on.

Fig.3 Power Dissipation vs. Ambient Temperature



Note) Oblique line prtion:Overheat protection may operate in this area

Fig.5 Overcurrent Protection Characteristics (PQ5EV5)

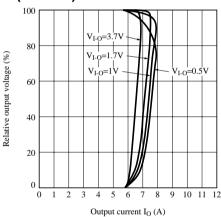


Fig.7 Reference Voltage Fluctuation vs. Junction Temperature

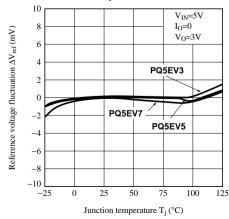


Fig.4 Overcurrent Protection Characteristics (PQ5EV3)

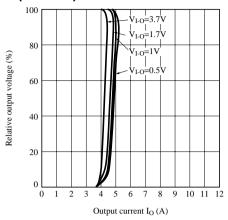


Fig.6 Overcurrent Protection Characteristics (PQ5EV7)

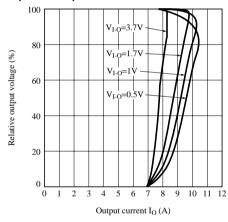


Fig.8 Output Voltage vs. Input Voltage (PQ5EV3)

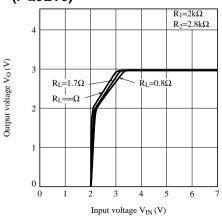


Fig.9 Output Voltage vs. Input Voltage (PQ5EV5)

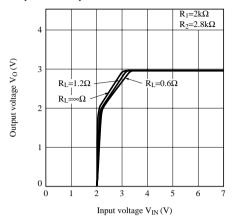


Fig.11 Circuit Operating Current vs. Input Voltage (PQ5EV3)

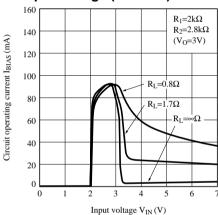


Fig.13 Circuit Operating Current vs. Input Voltage (PQ5EV7)

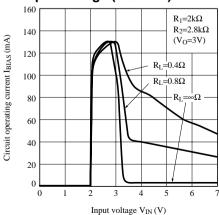


Fig.10 Output Voltage vs. Input Voltage (PQ5EV7)

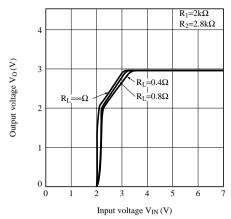


Fig.12 Circuit Operating Current vs. Input Voltage (PQ5EV5)

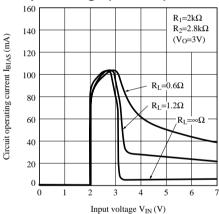


Fig.14 Dropout Voltage vs. Junction Temperature

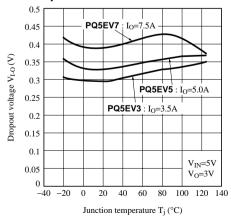


Fig.15 ON-OFF Threshold Voltage vs. Junction Temperature

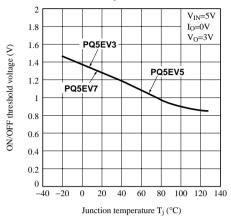


Fig.17 Ripple Rejection vs. Input Ripple Frequency

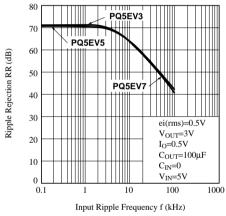
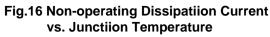


Fig.19 External Connection



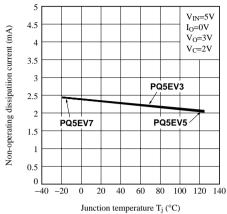
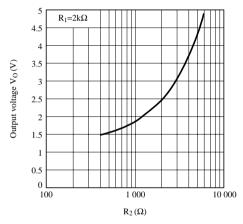
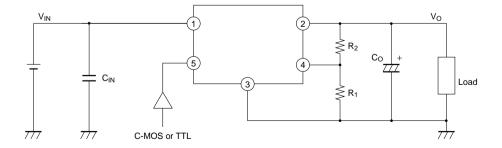


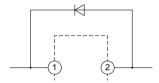
Fig.18 Output Voltage Adjustment Characteristics





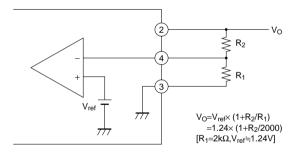
■ Precautions for Use

- 1. The connecting wiring of C_O and each terminal must be as short as possible. Owing to type, value and wiring condition of capacitor, it may oscillate. Confirm the output waveform under the actual condition before using.
- ON/OFF control terminal (3) is compatible with LS-TTL. It enables to be directly drive by TTL or C-MOS standard logic (RCA4000 series). If ON/OFF control terminal is not used, it is recommended to directly connect applicable terminals with input terminal.
- 3. If voltage is applied under the conditions that the device pin is connected divergently or reversely, the deterioration of characteristics or damage may occur. Never allow improper mounting.
- 4. If voltage exceeding the voltage of DC input terminal 1 is applied to the output terminal 2, the element may be damaged. Especially when the DC input terminal 1 is short-circuited to the GND in ordinary operating state, charges accumulated in the output capacitor C_0 flow to the input side, causing damage to the element. In this case, connect the ordinary silicon diode as shown in the figure.



■ Adjustment of Output Voltage

1. Output voltage is able to set (1.5V to 5V) when resistors R_1 , R_2 are attached to 2, 3, 4 terminals. As for the external resistors to set output voltage, refer to the following figure and Fig.18.



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