SKYWORKS

DATA SHEET

SKY65170-21: 860 – 960 MHz, 0.5 W High Power Power Amplifier

Applications

- CDMA2000 cellular band
- WCDMA, Bands V and VIII
- Femtocell transmitters

Features

- Wideband frequency operation: 860 to 960 MHz
- High gain: > 30 dB
- 0P1dB: > +27 dBm
- ACPR $\leq -51~dBc$ for $\mbox{Pout} \geq +18~dBm$
- ACLR \leq –45 dBc for Pout \geq +18 dBm
- Single DC supply: 5 V
- Internal RF match and bias circuits
- PA on/off control voltage
- Small footprint, MCM (20-pin, 6 x 6 mm) SMT package (MSL3, 260 °C per JEDEC J-STD-020)



Skyworks Pb-free products are compliant with all applicable legislation. For additional information, refer to *Skyworks Definition of Lead (Pb)-Free*, document number SQ04-0073.

Description

Skyworks SKY65170-21 is a fully-matched, 0.5 W Power Amplifier (PA) with high efficiency, designed for use in the 860 to 960 MHz band.

The device is fabricated using Skyworks high reliability Indium Gallium Phosphide (InGaP) Heterojunction Bipolar Transistor (HBT) technology. This process allows for single supply operation while maintaining high efficiency and good linearity.

The SKY65170-21 contains all of the active bias and RF matching circuits, which make it easy to integrate into 50 Ω RF systems. The internal input and output match are optimized for gain, 1 dB Output Compression Point (OP1dB), Adjacent Channel Leakage Ratio (ACLR), Adjacent Channel Power Ratio (ACPR), and efficiency when integrated in a 50 Ω system.

The primary bias to the SKY65170-21 can be supplied directly from a single 5 VDC supply. The PA is designed to operate with lower current by reducing the bias voltage.

The device is internally matched and mounted in a 20-pin, 6 x 6 mm Multi-Chip Module (MCM) Surface-Mounted Technology (SMT) package, which allows for a highly manufacturable low cost solution. The device package and pinout for the 20-pin MCM are shown in Figure 1. A block diagram of the SKY65170-21 is shown in Figure 2.



Figure 1. SKY65170-21 Pinout – 20-Pin MCM (Top View)



Figure 2. SKY65170-21 Block Diagram

Technical Description

The SKY65170-21 PA contains all of the needed RF matching and DC biasing circuits. The device is a two-stage, HBT InGaP device optimized for high linearity and power efficiency. These features make the device suitable for wideband digital applications, where PA linearity and power consumption are of critical importance (e.g., femtocell and infrastructure applications).

The device has been characterized with a standard CDMA2000 modulated signal. Under these stringent test conditions, the device exhibits excellent spectral purity and power efficiency.

Electrical and Mechanical Specifications

Signal pin assignments and functional pin descriptions are described in Table 1. The absolute maximum ratings of the SKY65170-21 are provided in Table 2. Electrical specifications are provided in Table 3.

Typical performance characteristics of the SKY65170-21 are illustrated in Figures 3 through 7.

Pin #	Name	Description	Pin #	Name	Description
1	GND	Ground	11	GND	Ground
2	RF_IN	RF input	12	RF_OUT	RF output
3	GND	Ground	13	GND	Ground
4	PA_EN	PA enable	14	GND	Ground
5	GND	Ground	15	VCC2	Stage 2 collector voltage, 5 V
6	VC_BIAS	Bias voltage, 5 V	16	GND	Ground
7	GND	Ground	17	N/C	No connection
8	V_REF1	Bias reference 1 voltage, 5 V	18	GND	Ground
9	GND	Ground	19	VCC1	Stage 1 collector voltage, 5 V
10	V_REF2	Bias reference 2 voltage, 5 V	20	N/C	No connection

Table 1. SKY65170-21	Signal Descriptions
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Parameter	Symbol	Minimum	Maximum	Units
RF output power	Роит		+29	dBm
Supply voltage	VCC1, VCC2, V_REF1, V_REF2, VC_BIAS		5.5	V
PA enable voltage	PA_EN	3	5	V
Total supply current	lcc		700	mA
Power dissipation	Po		1.5	W
Storage temperature	Тѕт	-65	+125	°C
Junction temperature	TJ		+150	°C
Thermal resistance	Θıc		30	°C/W

Table 2. SKY65170-21 Absolute Maximum Ratings (Note 1)

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SKY65170-21 Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Units
Frequency range	f	860		960	MHz
Supply voltage (Note 1)	VCC1, VCC2	4.75	5.00	5.25	V
Bias voltage (Note 1)	VC_BIAS	4.75	5.00	5.25	V
Reference voltage (Note 1)	V_REF1, V_REF2		2.60	2.75	V
Reference current	I_ref1, I_ref2		12		mA
PA enable current	IPA_EN		<1		mA
PA enable voltage (Note 1)	PA_EN	3		5	V
Case operating temperature	Tc	-40	+25	+85	°C

Note 1: Voltage levels measured at the pins of the package. The Evaluation Board supply voltage levels may be different. Refer to the Evaluation Board schematic diagram in this Data Sheet.

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Output power @ 1 dB compression	0P1dB		+27	+28		dBm
Small signal gain	IS211	Pıℕ = −15 dBm	30	32		dB
Adjacent Channel Power Ratio	ACPR1	@750 kHz, Роит = +18 dBm		-55	-48	dBc
	ACPR2	@1.98 MHz, Pout = +18 dBm		-74	63	dBc
Adjacent Channel Leakage Ratio	ACLR	$P_{OUT} = +18 \text{ dBm}$, with WCDMA test model 1, 64 DPCH		-45		dBc
3 rd Order Output Intercept Point	OIP3	PTONE = +15 dBm, frequency spacing = 1 MHz		+45		dBm
Quiescent Current	la	No RF input		200		mA
Operating current	lcc	Роит = +18 dBm		220		mA
Noise Figure	NF			6.5		dB
Input return loss	IS11I	Pıℕ = −15 dBm	10	14		dB
Output return loss	IS22I	Pıℕ = −15 dBm	9	12		dB
Voltage Standing Wave Ratio for stable operation	VSWR	Pout = +20 dBm with WCDMA test model 1, 64 DPCH		8:1	-	
Leakage current	ILEAKAGE	$PA_EN = 0 V$		1.2		μA

Table 4. SKY65170-21 Electrical Specifications (Note 1) (VCC1 = VCC2 = V_REF1 = V_REF2 = VC_BIAS = 5 V, Tc = +25 °C, f = 880 MHz, PA_EN = 5 V, CW, Unless Otherwise Noted)

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Typical Performance Characteristics

(VCC1 = VCC2 = V_REF1 = V_REF2 = VC_BIAS = 5 V, Tc = +25 °C, f = 880 MHz, PA_EN = 5 V, CW, Unless Otherwise Noted)







Figure 5. Current vs Output Power (CW)



Figure 7. ACLR Limited Output Power vs Frequency (Band V, WCDMA Test Model 1, 64 DPCH, ACLR = -45 dBc)



Figure 4. Gain vs Output Power



Figure 6. ACPR vs Output Power (CDMA2000)

Evaluation Board Description

The SKY65170-21 Evaluation Board is used to test the performance of the SKY65170-21 High Power PA. A schematic diagram of the SKY65170-21 Evaluation Board is shown in Figure 8. An assembly drawing for the Evaluation Board is shown in Figure 9 and the layer detail is provided in Figure 10. The layer detail physical characteristics are noted in Figure 11.

Package Dimensions

The PCB layout footprint for the SKY65170-21 is provided in Figure 12. Package dimensions for the 20-pin MCM are shown in Figure 13, and tape and reel dimensions are provided in Figure 14.

Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY65170-21 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to Skyworks Application Note, *PCB Design and SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



Figure 8. SKY65170-21 Evaluation Board Schematic



Figure 9. SKY65170-21 Evaluation Board Assembly Drawing



Figure 10. SKY65170-21 Evaluation Board Layer Detail



Figure 11. Layer Detail Physical Characteristics



Figure 12. SKY65170-21 PCB Layout Footprint

S2763



Figure 13. SKY65170-21 20-Pin MCM Package Dimensions



For all other 6 x 6 mm MCM/RFLGA products, pin 1 orientation is in top right corner.

S1183

Figure 14. SKY65170-21 20-Pin MCM Tape and Reel Dimensions

Ordering Information

Model Name		Manufacturing Part Number	Evaluation Board Part Number
ſ	SKY65170-21 860-960 MHz, 0.5 W High Power Power Amplifier	SKY65170-21	TW18-D370

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