5LP01C

P-Channel Small Signal MOSFET -50V, -0.07A, 23Ω, Single CP



- Low ON-resistance
- Ultrahigh-speed switching
- 1.5V drive
- Halogen free compliance

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain to Source Voltage	VDSS		-50	V
Gate to Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		-0.07	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	-0.28	А
Allowable Power Dissipation	PD		0.25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

This product is designed to "ESD immunity $< 200V^*$ ", so please take care when handling.

* Machine Model

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ) 7013A-013



Ordering & Package Information

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Device	Package	Shipping	memo					
5LP01C-TB-E	CP SC-59, TO-236, SOT-23, TO-236AB	3,000 pcs./reel	Pb-Free					
5LP01C-TB-H	CP SC-59, TO-236, SOT-23, TO-236AB	3,000 pcs./reel	Pb-Free and Halogen Free					

Packing Type: TB







Electrical Connection





http://onsemi.com

Electrical Characteristics at Ta=25°C

Parameter	Symbol		Ratings			L Incit
		Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID= -1mA, VGS=0V	-50			V
Zero-Gate Voltage Drain Current	IDSS	$V_{DS} = -50V$, $V_{GS} = 0V$			-1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} = -10V, I _D = -100µA	-0.4		-1.4	V
Forward Transfer Admittance	yfs	V _{DS} = -10V, I _D = -40mA	70	100		mS
Static Drain to Source On-State Resistance	R _{DS} (on)1	$I_{D} = -40 \text{mA}, V_{GS} = -4 \text{V}$		18	23	Ω
	R _{DS} (on)2	I _D = -20mA, V _{GS} = -2.5V		20	28	Ω
	R _{DS} (on)3	ID= -5mA, VGS= -1.5V		30	60	Ω
Input Capacitance	Ciss	 V _{DS} = -10V, f=1MHz		7.4		pF
Output Capacitance	Coss			4.2		pF
Reverse Transfer Capacitance	Crss			1.3		pF
Turn-ON Delay Time	t _d (on)			20		ns
Rise Time	tr			35		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		160		ns
Fall Time	tf]		150		ns
Total Gate Charge	Qg			1.40		nC
Gate to Source Charge	Qgs	V _{DS} = -10V, V _{GS} = -10V, I _D = -70mA		0.16		nC
Gate to Drain "Miller" Charge	Qgd]		0.23		nC
Diode Forward Voltage	V _{SD}	I _S = -70mA, V _{GS} =0V		-0.85	-1.2	V

Switching Time Test Circuit









Outline Drawing 5LP01C-TB-E, 5LP01C-TB-H



Land Pattern Example



Note on usage : Since the 5LP01C is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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