

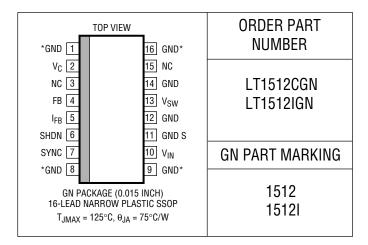
## SPECIFICATION NOTICE LT1512

September 1998

The **LT®1512** is now available in the **GN** package (16-pin narrow SSOP). Functional changes are described below. All other specifications remain unchanged. For complete specifications, typical performance characteristics and applications information, please see the **LT1512** data sheet.

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## PACKAGE/ORDER INFORMATION



\* The four corner pins (1, 8, 9, 16) are fused to the internal leadframe to lower thermal resistance. These pins should be connected to copper plane areas on the board for proper heat sinking. The  $\theta_{JA}$  value is preliminary.

## PIN FUNCTIONS

The GN package has the Shutdown and Synchronization (S/S) functions brought out to separate pins. The functions remain the same.

**SHDN (Pin 6):** The shutdown pin will default to a high ON state when floated. A logic state will shut down the charger to a micropower state. The shutdown pin is logic level compatible but can be tied to  $V_{\text{IN}}$  if desired.

**SYNC (Pin 7):** The SYNC pin can be used to synchronize the switching frequency of the device. Driving the SYNC pin with a continuous switching logic signal of 600kHz to 800kHz will synchronize switching frequency to the external signal. If the synchronization function is not used, the SYNC pin should be grounded.

For further information regarding this specification notice contact:

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